



USER MANUAL

EPIA-P900

Pico-ITX embedded board



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This equipment has been tested and found to comply with the limits for a class A digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his personal expense.

Notice 1

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Notice 3

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With FCC Standards
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- ☐ Do not re-use, recharge, or reheat an old battery.
- ☐ Do not attempt to force open the battery.
- ☐ Do not discard used batteries with regular trash.
- ☐ Discard used batteries according to local regulations.



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- ☐ Always read the safety instructions carefully.
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- ☐ All cautions and warnings on the equipment should be noted.
- ☐ Keep this equipment away from humidity.
- ☐ Lay this equipment on a reliable flat surface before setting it up.
- ☐ Make sure the voltage of the power source and adjust properly 110/220V before connecting the equipment to the power inlet.
- ☐ Place the power cord in such a way that people cannot step on it.
- ☐ Always unplug the power cord before inserting any add-on card or module.
- ☐ If any of the following situations arises, get the equipment checked by authorized service personnel:
 - The power cord or plug is damaged.
 - Liquid has penetrated into the equipment.
 - The equipment has been exposed to moisture.
 - The equipment has not worked well or you cannot get it work according to User's Manual.
 - The equipment has dropped and damaged.
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- ☐ Do not leave this equipment in direct sunlight.
- ☐ Never pour any liquid into the opening. Liquid can cause damage or electrical shock.
- ☐ Do not place anything over the power cord.
- ☐ Do not cover the ventilation holes. The openings on the enclosure protect the equipment from overheating

Box Contents

- ☐ 1 x EPIA-P900 board
- ☐ 1 x P830-A I/O card (pre-assembled companion card)
- ☐ 1 x SATA cable (P/N: 99G33-240103)
- ☐ 1 x SATA power cable (P/N: 99G33-020985)
- ☐ 1 x DC power cable (P/N: 99G33-250024)

Optional Accessories

I/O Expansion Cards

Part Number	Description
LPC-01	Expansion module for four COM ports
LPC-02	Expansion module for four COM ports (5V or 12V)
LPC-04	Expansion module for two or four COM ports (5V or 12V)

Wireless Modules

Part Number	Description
00GO27100BU2B0D0	VNT9271 IEEE 802.11b/g/n USB Wi-Fi dongle
EMIO-1533-00A2	VNT9271 IEEE 802.11b/g/n USB Wi-Fi module with assembly kit and antenna.
EMIO-5531-00A1	VAB-820-W IEEE 802.11b/g/n USB Wi-Fi & Bluetooth module with assembly kit and antenna

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1. Product Overview

The VIA EPIA-P900 Pico-ITX mainboard is a compact native x86 mainboard optimized for systems in embedded and multimedia applications. It can also be used for various domain applications such as medical PC, industrial PC, and etc. The mainboard is based on the VIA VX900H Unified Digital Media IGP chipset that features the VIA C-9 HD DX9 3D/2D graphics with video accelerators for rich digital media performance.

The VIA EPIA-P900 mainboard has integrated HDMI® port and comes with pre-installed I/O companion card called P830-A, and an optional P830-B expansion card. The P830-A I/O companion card is connected through onboard pin headers carrying the back panel I/O such as VGA port, Gigabit Ethernet port and USB 2.0 ports. The P830-B expansion card provides two miniPCIe x1 slots.

The VIA EPIA-P900 has a powerful, secure, and efficient 1.0GHz VIA Eden® X2 processor. The VIA Eden® X2 processor includes the VIA AES Security Engine, VIA CoolStream™ Architecture and VIA PowerSaver™ Technology.

The VIA EPIA-P900 includes one DDR3 800/1066 SODIMM slot that support up to 4GB memory size. The VIA EPIA-P900 provides support for high fidelity audio with its included VIA VT2021 High Definition Audio codec. In addition it supports two SATA 3Gbps storage devices, and fully compatible with Microsoft® and Linux operating systems.

1.1. Key Features and Benefits

1.1.1. VIA Eden® X2 Processor

The VIA Eden® X2 is a 64-bit superscalar x86 dual core processor based on a 40 nanometer process technology. Packed into an ultra compact NanoBGA2 package (measuring 21mm x 21mm), it delivers an energy-efficient yet powerful performance, with cool and quiet operation. The VIA Eden® X2 is ideal for embedded system applications such as industrial PCs, test machines, measuring equipment, digital signage, medical PCs, monitoring systems, gaming machines, in-vehicle entertainment, etc.



Note:

For Windows 7 and Windows Server 2008 R2 users only:

If encounter the issue such as the operating system recognizing the VIA Dual-Core CPU as two processors instead of one processor with two cores. Download and install the hotfix released by Microsoft to address this issue. The downloadable hotfix is available at <http://support.microsoft.com/kb/2502664>

1.1.2. VIA VX900H Chipset

The VIA VX900H Unified Digital Media Chipset is designed to enable high quality digital video streaming and DVD playback in a new generation of fanless, small form factor PCs and IA devices. The VIA VX900H features VIA C-9 HD DX9 3D/2D graphics with video decoding acceleration, DDR3 1066/800 support, motion compensation and dual display support to ensure a rich overall entertainment experience.

1.1.3. Expansion Option

The EPIA-P900 further proves its versatility by providing an expansion option connector that uses a "PCIe and USB 2.0 combination connector" for P830-B expansion card. The P830-B expansion card provides two miniPCIe x1 slots.

The companies using the EPIA-P900 with P830-B expansion card obtain the maximum benefits and enable to slowly roll out upgrades as necessary instead of having to replace everything all at once.

1.2. Product Specifications

- **Processor**
 - 1.0GHz VIA Eden® X2
 - Supports 800MHz Front Side Bus
 - x86 and x64 compatible
 - 21mm x 21mm FCBGA
- **Chipset**
 - VIA VX900H Unified Digital Media IGP chipset
 - 31mm x 31mm FCBGA
- **Graphics**
 - Integrated VIA C-9 HD DX9 3D/2D graphics with MPEG-2, WMV9, VC1 and H.264 video decoding acceleration
- **System Memory**
 - 1 x SODIMM slot supporting DDR3 800/1066
 - Supports up to 4GB memory size
- **Onboard Peripherals**
 - **Serial ATA**
 - Supports up to 3Gbps
 - **Onboard LAN**
 - VIA VT6130 PCIe Gigabit Ethernet controller
 - **Onboard Audio**
 - VIA VT2021 High Definition Audio Codec
 - **Onboard Super I/O**
 - Fintek F81801U-I Super I/O controller
- **Onboard I/O**
 - 1 x USB 2.0 pin headers for 5 addition USB 2.0 ports and 1 USB 2.0 device port
 - 1 x Single-channel 18/24-bit LVDS panel connector
 - 1 x VGA and USB combination pin header (supports two USB 2.0 ports and one VGA port)
 - 1 x Gigabit Ethernet pin header
 - 1 x LPC, SMBus and GPIO (4 GPI + 4 GPO) combination pin header
 - 1 x Front panel and PS2 combination pin header
 - 1 x Front audio pin header for Line-out, Line-in and Mic-in
 - 1 x PCIe and USB combination connector for P830-B expansion module
 - 2 x SATA connectors
 - 1 x SATA power connector
 - 1 x CPU fan connector
 - 2 x UART pin headers
 - 1 x SPI flash connector
 - 1 x LVDS panel power select jumper (5V/3.3V)
 - 1 x LVDS backlight power select jumper (5V/12V)
 - 1 x CMOS external battery connector
 - 1 x DC-in connector (+12V±5%)

- **Back Panel I/O**
 - 1 x HDMI® port
 - 1 x VGA port (on P830-A)
 - 1 x Gigabit Ethernet port (on P830-A)
 - 2 x USB 2.0 ports (on P830-A)
- **BIOS**
 - AMI BIOS
 - 8Mbit SPI flash memory
- **Supported Operating System**
 - Microsoft Windows 7
 - Microsoft Windows Embedded Standard/Compact
 - Linux
- **System Monitoring & Management**
 - Wake-on-LAN
 - Keyboard-Power-on, Timer-Power-on
 - System Power Management
 - AC power failure recovery
 - Watchdog Timer
 - Smart Fan for CPU fan
- **Operating Temperature**
 - 0°C ~ 60°C
- **Operating Humidity**
 - 0% ~ 95% (relative humidity; non-condensing)
- **Form Factor**
 - Pico-ITX (10cm x 7.2cm)
- **Compliance**
 - CE
 - FCC
 - RoHS


Note:

As the operating temperature provided in the specifications is a result of the test performed in VIA's chamber, a number of variables can influence this result. Please note that the working temperature may vary depending on the actual situation and environment. It is highly suggested to execute a solid testing and take all the variables into consideration when building the system. Please ensure that the system runs well under the operating temperature in terms of application.

1.3. Layout Diagram

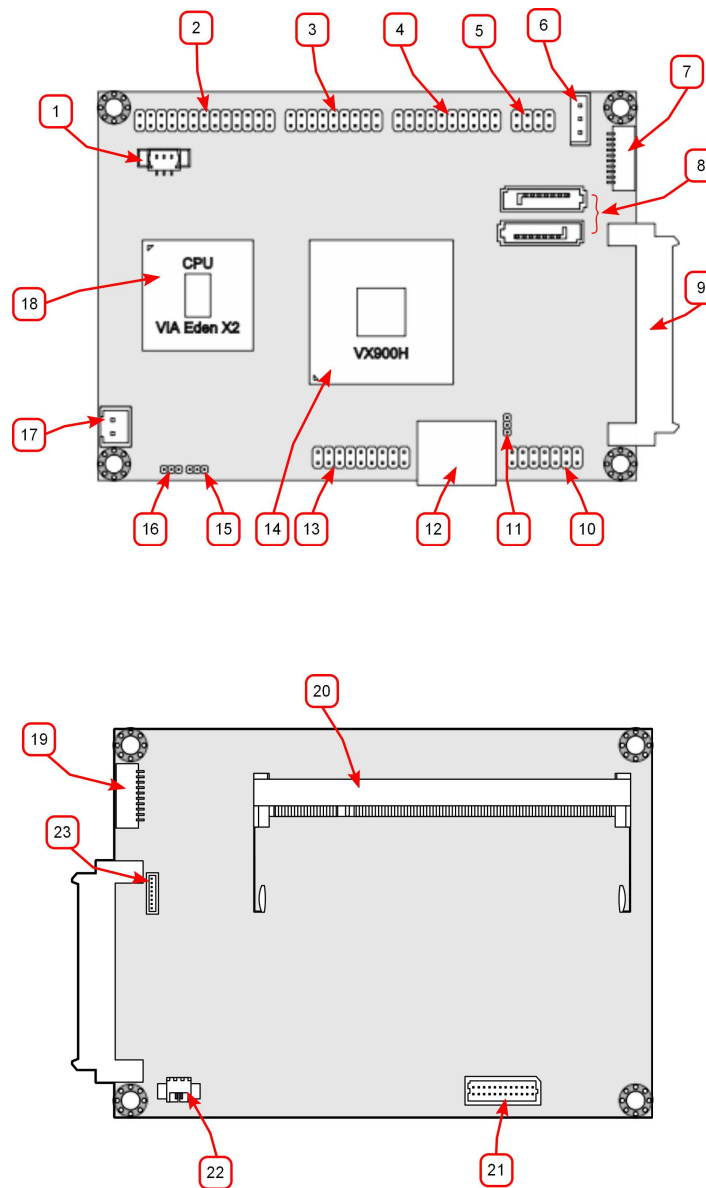


Figure 1: Layout diagram of the EPIA-P900 mainboard (top and bottom view)

Item	Description
1	FAN1: CPU fan
2	CN5: LPC, SMBus, and GPIO combination pin header
3	CN4: Front panel and PS2 combination pin header
4	CN2: USB, and USB Device combination pin header
5	CN1: Front audio pin header
6	PWR2: SATA power connector
7	J1: UART connector 2
8	SATA1, SATA2: SATA connectors
9	CN6: PCIe and USB 2.0 combination connector
10	CN3: Gigabit Ethernet pin header
11	JM1: Clear CMOS jumper
12	HDMI1: HDMI® port
13	VGA_USB1: VGA and USB combination pin header
14	VX900H chipset
15	JM2: LVDS panel power select jumper
16	JM3: LVDS backlight power select jumper
17	PWR1: DC-in connector
18	VIA Eden® X2 CPU
19	J2: UART connector 1
20	SODIMM1: DDR3 800/1066 SODIMM slot
21	LVDS1: LVDS panel connector
22	BAT1: CMOS battery connector
23	J3: SPI flash connector

Table 1: Layout diagram description table of the EPIA-P900 mainboard

1.4. Product Dimensions

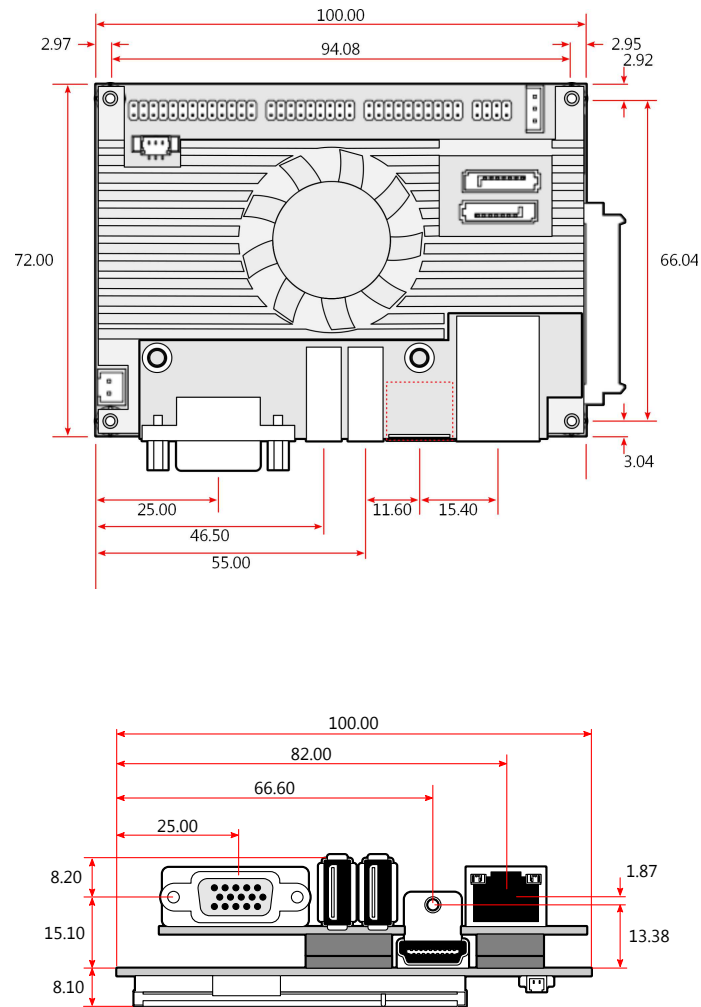


Figure 2: Mounting holes and dimensions of the EPIA-P900



Note:

Each measurement are measured in unit of mm (millimeters)

1.5. Height Distribution

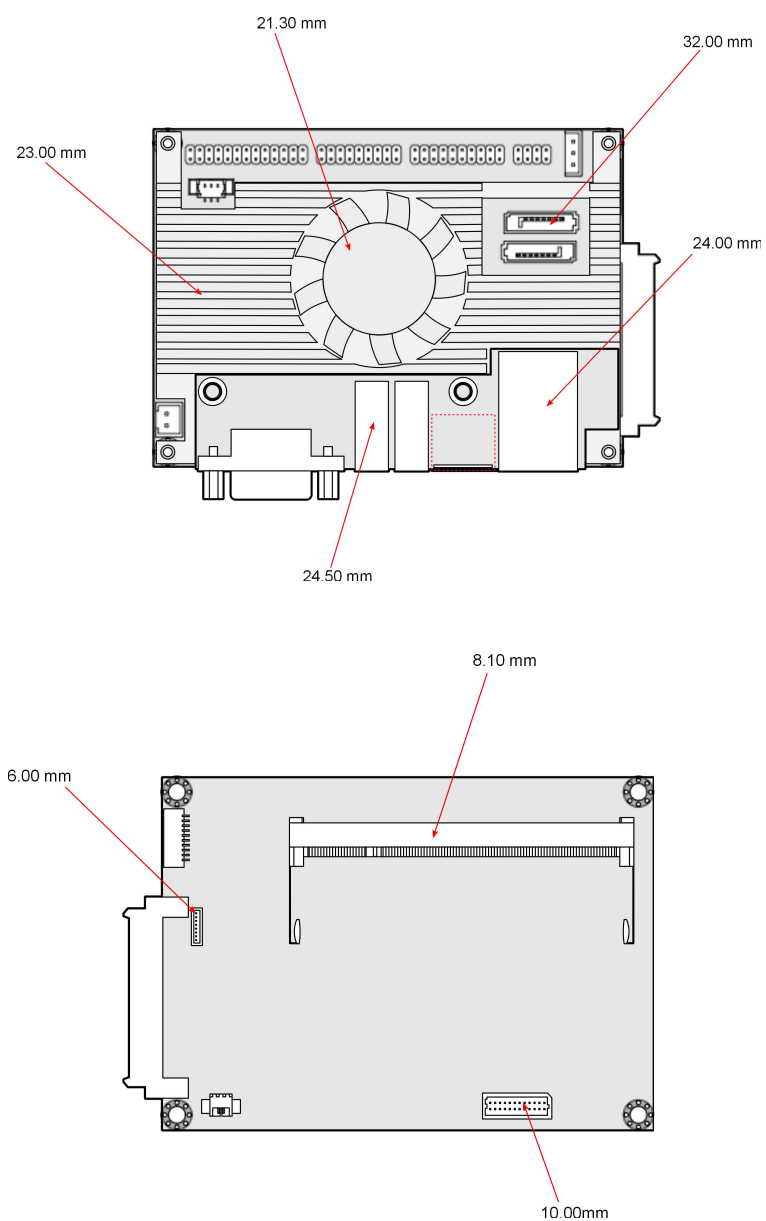


Figure 3: Height distribution of the EPIA-P900 mainboard

2. I/O Interface

The VIA EPIA-P900 has a wide selection of interfaces. It includes a selection of frequently used ports as part of the external I/O coastline.

2.1. External I/O Ports

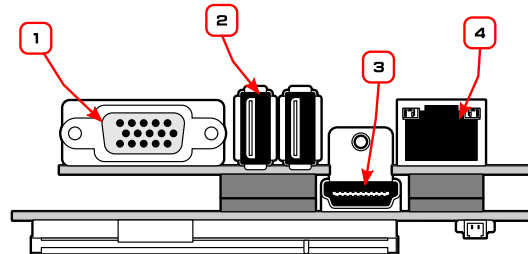


Figure 4: External I/O ports

Item	Description
1	VGA1: VGA port (on P830-A)
2	USB1, USB2: USB 2.0 ports (on P830-A)
3	HDMI1: HDMI® port
4	RJ1: Gigabit Ethernet port (on P830-A)

Table 2: Layout diagram description table of external I/O ports

2.1.1. HDMI® Port

The integrated 19-pin HDMI® port uses an HDMI® Type A receptacle connector as defined in the HDMI® specification. The HDMI® port is for connecting to HDMI® displays. The pinout of the HDMI® port is shown below.

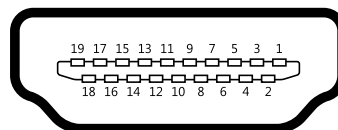


Figure 5: HDMI® port diagram

Pin	Signal	Pin	Signal
1	HDMITX2+	2	GND
3	HDMITX2-	4	HDMITX1+
5	GND	6	HDMITX1-
7	HDMITX0+	8	GND
9	HDMITX0-	10	HDMITXC+
11	GND	12	HDMITXC-
13	NC	14	NC
15	SPCLK	16	SPDAT
17	GND	18	PVDD5
19	-DP1_HPD		

Table 3: HDMI® port pinout

2.1.2. VGA Port

The 15-pin VGA port uses a female DE-15 connector. The VGA port is for connecting to analog displays. The pinout of the VGA port is shown below.

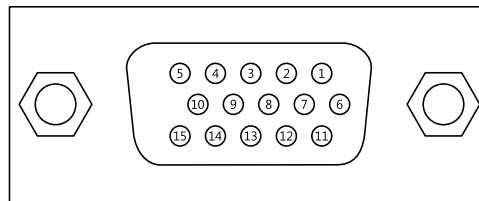


Figure 6: VGA port diagram

Pin	Signal	Pin	Signal
1	VGA-R	9	+5VCRT
2	VGA-G	10	GND
3	VGA-B	11	NC
4	NC	12	VGA-SPD
5	GND	13	VGA_HS
6	GND	14	VGA_VS
7	GND	15	VGA-SPCLK
8	GND		

Table 4: VGA port pinout

2.1.3. USB 2.0 Port

There are two integrated USB 2.0 ports located below the Gigabit Ethernet port at the external I/O panel. The USB 2.0 interface port gives complete Plug and Play and hot swap capability for external devices and it complies with USB UHCI, rev. 2.0. Each USB port is using the USB Type A receptacle connector. The pinout of the typical USB port is shown below.

Pin	Signal
1	+5VSUS
2	Data-
3	Data+
4	GND

Table 5: USB 2.0 port pinout

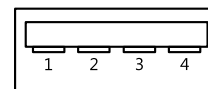


Figure 7: USB 2.0 port diagram

2.1.4. Gigabit Ethernet Port

The integrated 8-pin Gigabit Ethernet port is using an 8 Position 8 Contact (8P8C) receptacle connector (commonly referred to as RJ-45). The Gigabit Ethernet ports are controlled by VIA Gigabit Ethernet controller. The pinout of the Gigabit Ethernet port is shown below.

Pin	Signal
1	Signal pair 1+
2	Signal pair 1-
3	Signal pair 2+
4	Signal pair 3+
5	Signal pair 3-
6	Signal pair 2-
7	Signal pair 4+
8	Signal pair 4-

Table 6: Gigabit Ethernet port pinout

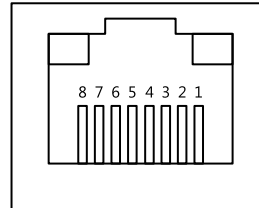


Figure 8: Gigabit Ethernet port diagram

The RJ-45 port has two individual LED indicators located on the front side to show its Active/Link status and Speed status.

	Link LED (Left LED on RJ-45 connector)	Active LED (Right LED on RJ-45 connector)
Link Off	Off	Off
Speed_10Mbit	The LED is always On in either Green or Orange colors	Flash in Yellow color
Speed_100Mbit	The LED is always On in Green color	Flash in Yellow color
Speed_1000Mbit	The LED is always On in Orange color	Flash in Yellow color

Table 7: Gigabit Ethernet LED color definition

2.2. Onboard I/O

2.2.1. LVDS Panel Connector

The mainboard has one 24-pin LVDS panel connector on the bottom side. The onboard LVDS panel connector allows to connect the panel's LVDS cable to support the single-channel 18-bit/24-bit display. Backlight controls are integrated into the LVDS panel connector pinout. The LVDS panel connector is labeled as "LVDS1". The pinout of the connector is shown below.

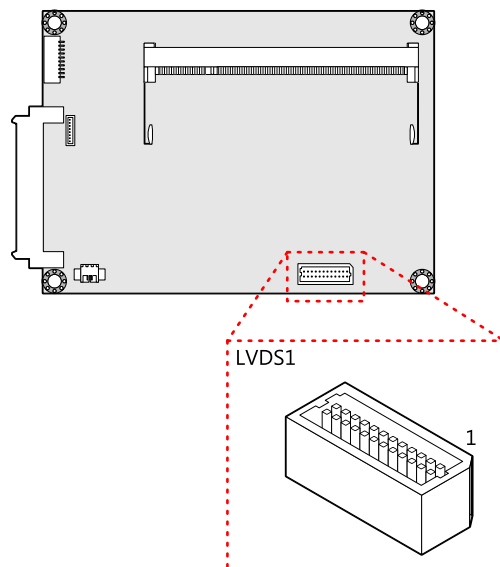


Figure 9: LVDS panel connector diagram

Pin	Signal	Pin	Signal
1	LVDS0-	2	LVDS1-
3	LVDS0+	4	LVDS1+
5	GND	6	GND
7	PVDD2	8	LVDS2-
9	PVDD2	10	LVDS2+
11	LCD1_DATA	12	GND
13	LCD1_CLK	14	LVDSCLK+
15	GND	16	LVDSCLK-
17	VDD_BL	18	GND
19	VDD_BL	20	LVDS3-
21	BLN_1	22	LVDS3+
23	BAK_ADJ	24	GND

Table 8: LVDS panel connector pinout

2.2.2. VGA and USB Combination Pin Header

The VGA and USB combination pin header block labeled as VGA_USB1 is used to connect to the P830-A companion card. The pin header block provides support for one VGA port and two USB 2.0 ports. The pinout of the VGA and USB combination pin header is shown below.

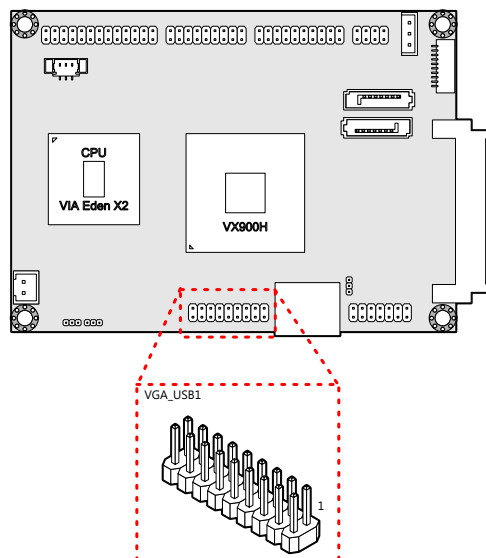


Figure 10: VGA and USB combination pin header diagram

Pin	Signal	Pin	Signal
1	REDN	2	+5VCRT
3	GREENN	4	GND
5	BLUEN	6	DDCDATAN
7	GND	8	DDCCLKN
9	VGP_IO	10	VS
11	HS	12	GND
13	+5VSUS_USB	14	GND
15	USBHP5-	16	USBHP4-
17	USBHP5+	18	USBHP4+

Table 9: VGA and USB combination pin header pinout

2.2.3. SATA Connectors

The two SATA connectors onboard can support up to 3Gbps transfer speeds. Both SATA connectors have a 7th pin¹ that can provide +5V power to a SATA Disk-on-Module (DOM). When a regular SATA hard drive is connected, the 7th pin will be a ground pin. The SATA connectors are labeled as “SATA1” and “SATA2”. The pinout of the SATA connectors are shown below.

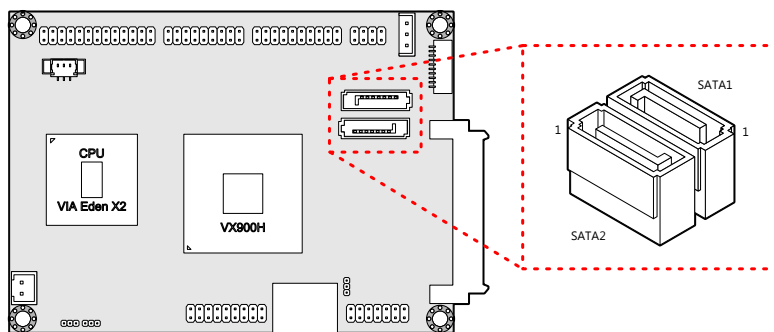


Figure 11: SATA connectors

SATA1		SATA2	
Pin	Signal	Pin	Signal
1	GND	1	GND
2	STXP_0	2	STXP_1
3	STXN_0	3	STXN_1
4	GND	4	GND
5	SRXN_0	5	SRXN_1
6	SRXP_0	6	SRXP_1
7	GND/+5V	7	GND/+5V

Table 10: SATA connector pinouts



Note:

1. The SATA connector pin 7 default setting is GND. The +5V supports is a factory option.

2.2.4. SATA Power Connector

The onboard SATA power connector provides both +5V and +12V directly through the mainboard to the SATA drives. The SATA power connector is labeled as "PWR2". The pinout of the SATA power connector is shown below.

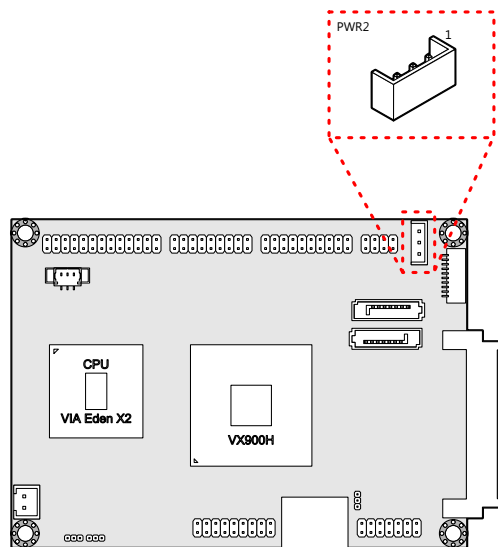


Figure 12: SATA connector

Pin	Signal
1	+5V
2	+12V
3	GND

Table 11: SATA connector pinout

2.2.5. USB and USB Device Combination Pin Header

The mainboard has one USB and USB Device combination pin header block enables the addition of five more USB 2.0 ports and one USB Device port. The pin header block is labeled as “CN2”. The pinout of the pin header is shown below.

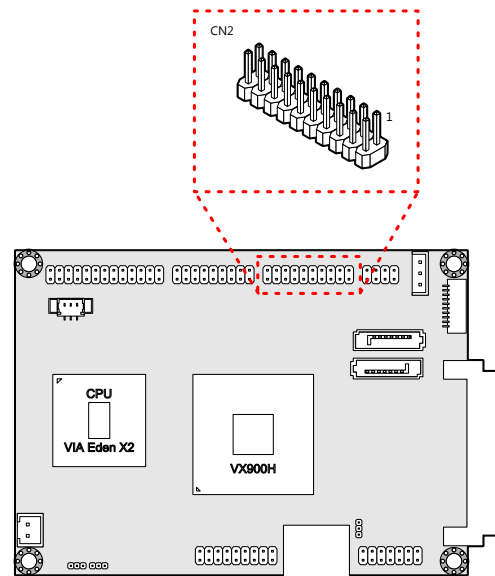


Figure 13: USB and USB device combination pin header diagram

Pin	Signal	Pin	Signal
1	GND	2	GND
3	GND	4	GND
5	USB_VD0+	6	USB_VD6+
7	USB_VD0-	8	USB_VD6-
9	+5VSUS_USB	10	+5VSUS_USB
11	USB_VD1-	12	USB_VD3-
13	USB_VD1+	14	USB_VD3+
15	+5VUSBD	16	+5VSUS_USB
17	USBDP+	18	USB_VD2+
19	USBDP-	20	USB_VD2-

Table 12: USB and USB device combination pin header pinout



Note:

USB Device port is a reserved feature. Contact sales for specific support.

2.2.6. Gigabit Ethernet Pin Header

The onboard Gigabit Ethernet pin header block is used to connect to the P830-A companion card. The pin header block labeled as “CN3” provides support for one Gigabit Ethernet port. The pin header pinout is shown below.

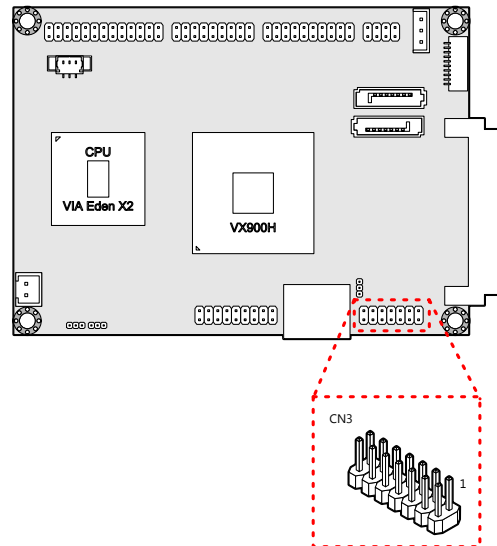


Figure 14: Gigabit Ethernet pin header diagram

Pin	Signal	Pin	Signal
1	VDDTXRX	2	AVDD33
3	TD2-	4	TD3-
5	TD2+	6	TD3+
7	TD0-	8	TD1-
9	TD0+	10	TD1+
11	GND	12	LED1
13	LED2	14	LNK_ACT

Table 13: Gigabit Ethernet pin header pinout

2.2.7. UART Connectors

The mainboard includes two UART connectors: UART connector 1 labeled as "J2" is the 12-pin connector on the bottom side. UART connector 2 labeled as "J1" is the 10-pin connector on the top side. The UART connectors can be used to attach an additional port for serial devices. The connector pinouts are shown below.

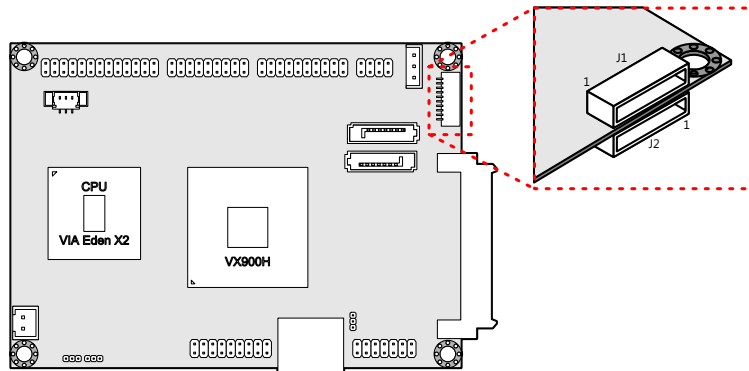


Figure 15: UART connectors diagram

UART 2 (J1)		UART 1 (J2)	
Pin	Signal	Pin	Signal
1	+5V	1	GND
2	SIN2	2	-LPCRST
3	SOUT2	3	SIO_GPIO16
4	-DCD2	4	CTS1
5	-RI2	5	-RTS1
6	GND	6	-DSR1
7	-DTR2	7	-DTR1
8	CTS2	8	SIN1
9	-RTS2	9	SOUT1
10	-DSR2	10	-DCD1
		11	-RI1
		12	+3.3V

Table 14: UART connectors pinouts

2.2.8. LPC, SMBus, and GPIO Combination Pin Header

The mainboard includes one LPC, SMBus, and GPIO combination pin header block labeled as “CN5”. The combination pin header is for connecting LPC, SMBus devices and General Purpose Input and Output. The pinout of the pin header is shown below.

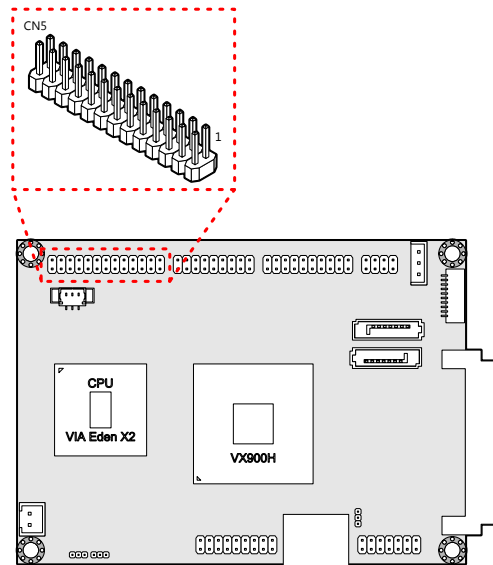


Figure 16: LPC, SMBus and GPIO combination pin header diagram

Pin	Signal	Pin	Signal
1	GND	2	LAD3
3	SIO_CLK1	4	LAD2
5	PCCLK2	6	LAD1
7	-LDRQ0	8	-LFRAME
9	SERIRQ	10	LAD0
11	NC	12	-PCIRST
13	SMB_CLK	14	SMB_DAT
15	+5V	16	+3.3V
17	-INTD/GPIO12	18	-RING/GPI8
19	-INTC/GPIO9	20	-THRM/GPI9
21	GPO12	22	-EXTSMI/GPI5
23	GPIO32	24	-BATLOW/GPI4
25	GND	26	GND

Table 15: LPC, SMBus and GPIO combination pin header pinout

2.2.9. SPI Flash Connector

The mainboard has one 8-pin SPI flash connector. The SPI (Serial Peripheral Interface) flash connector is used to connect to the SPI BIOS programming fixture for updating the SPI flash ROM. The connector is labeled as "J3". The pinout of the connector is shown below.

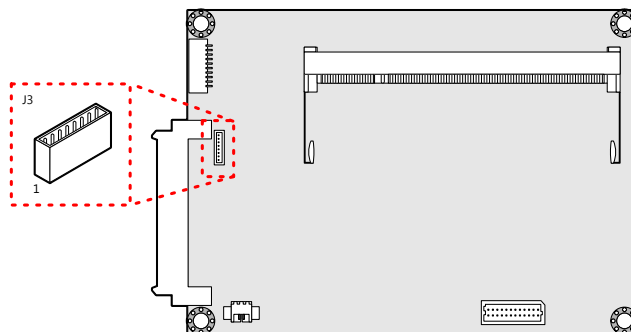


Figure 17: SPI flash connector diagram

Pin	Signal
1	NC
2	NC
3	MSPIDO
4	MSPIDI
5	MSPICLK
6	MSPISSO
7	GND
8	SPIVCC

Table 16: SPI flash connector pinout

2.2.10. CMOS Battery Connector

The mainboard is equipped with onboard CMOS battery connector used for connecting the external cable battery that provides power to the CMOS RAM. If disconnected all configurations in the CMOS RAM will be reset to factory defaults. The CMOS battery connector is labeled as "BAT1". The connector pinout is shown below.

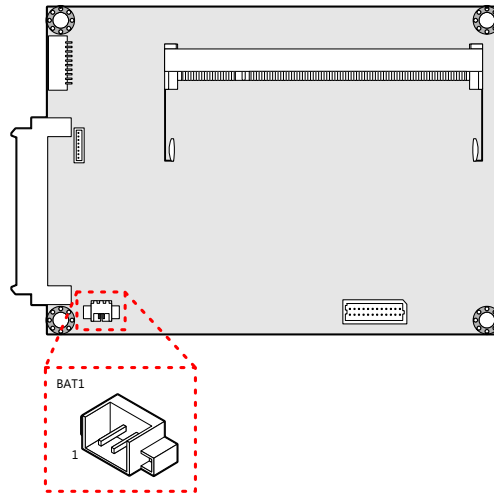


Figure 18: CMOS battery connector diagram

Pin	Signal
1	+VBAT
2	GND

Table 17: CMOS battery connector pinout

2.2.11. Front Panel and PS/2 Combination Pin Header

The mainboard includes one Front Panel and PS/2 combination pin header block labeled as "CN4". The front panel and PS/2 combination pin header is used to connect the power switch, reset switch, power LED, suspend LED, HDD LED, case speaker, PS/2 keyboard port and PS/2 mouse port. The pinout of the pin header is shown below.

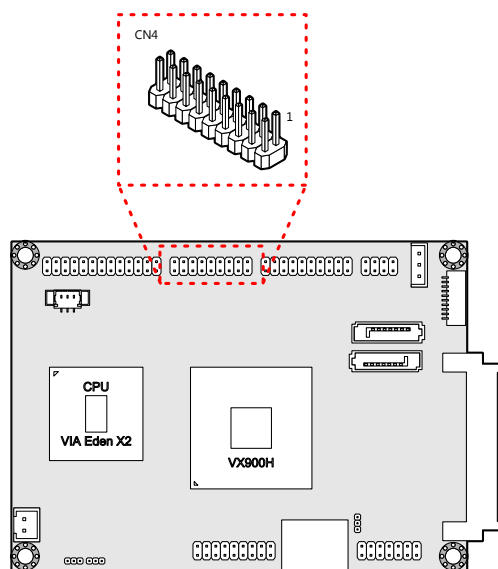


Figure 19: Front panel and PS/2 combination pin header diagram

Pin	Signal	Pin	Signal
1	+5VSUSLED1	2	+5VLED2
3	+5VSUSLED1	4	-HD_LED
5	GND	6	PW_BN-
7	SPEAK_BZ	8	GND
9	GND	10	RST_SW
11	-PWR_LED	12	GND
13	+5VSUS	14	GND
15	KBCK	16	KBDT
17	MSCK	18	MSDT

Table 18: Front panel and PS/2 combination pin header pinout

2.2.12. Front Audio Pin Header

The mainboard has front audio pin header that used for connecting the Line-out, Line-in and Mic-in jacks. The pin header is labeled as "CN1". The pinout of the pin header is shown below.

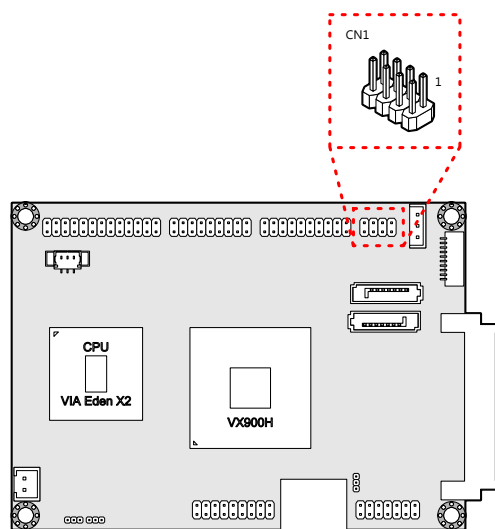


Figure 20: Front audio pin header diagram

Pin	Signal	Pin	Signal
1	LINER	2	GND_AUD
3	LINEL	4	MICINL
5	LINEOUTR	6	MICINR
7	LINEOUTL	8	SENSE A

Table 19: Front audio pin header pinout

2.2.13. CPU Fan Connector

The CPU fan connector onboard runs on +5V and maintain CPU cooling. The fan provides variable fan speeds controlled by the BIOS. The CPU fan connector is labeled as "FAN1". The pinout of the fan connector is shown below.

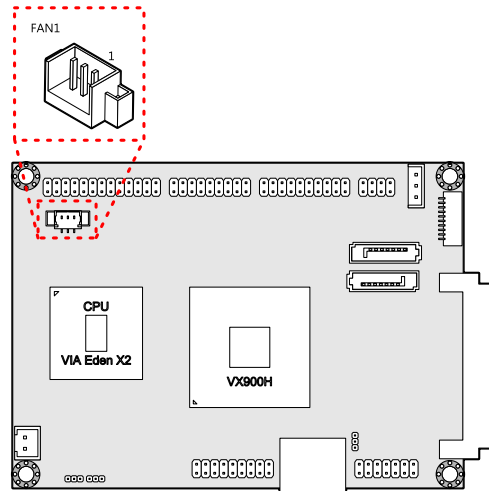


Figure 21: CPU fan connector diagram

Pin	Signal
1	FANIN1
2	+5V FANCTL
3	GND

Table 20: CPU fan connector pinout

2.2.14. DC-in Connector

The mainboard has an onboard DC-in 2-pin power connector to connect the DC-in power cable. The DC-in power connector is labeled as "PWR1". The pinout of the DC-in power connector is shown below.

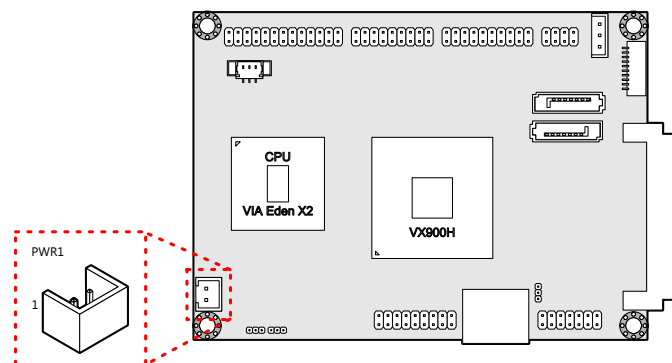


Figure 22: DC-in connector diagram

Pin	Signal
1	DC-in (+12V±5%)
2	GND

Table 21: DC-in connector pinout

3. Jumpers

Jumper Description

A jumper consists of pair conductive pins used to close in or bypass an electronic circuit to set up or configure particular feature using a jumper cap. The jumper cap is a small metal clip covered by plastic. It performs like a connecting bridge to short (connect) the pair of pins. The usual colors of the jumper cap are black/red/blue/white/yellow.

Jumper Setting

There are two settings of the jumper pin: “**Short** and **Open**”. The pins are “**Short**” when a jumper cap is placed on the pair of pins. The pins are “**Open**” if the jumper cap is removed.

In addition, there are jumpers that have three or more pins, and some pins are arranged in series. In case of a jumper with three pins, place the jumper cap on pin 1 and pin 2 or pin 2 and 3 to **Short** it.

Some jumper size is small or mounted on the crowded location on the board that makes it difficult to access. Therefore, using a long-nose plier in installing and removing the jumper cap is very helpful.

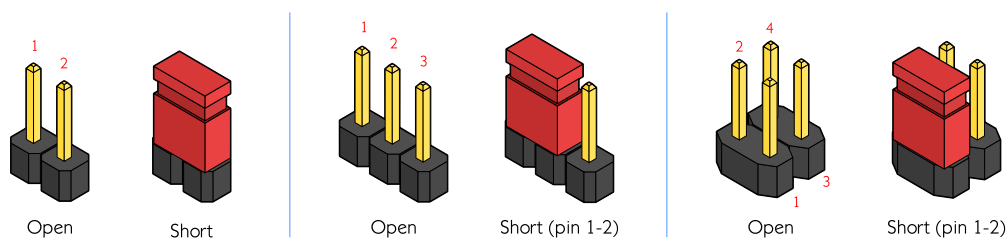


Figure 23: Jumper settings example



Caution:

Make sure to install the jumper cap on the correct pins. Installing it in the wrong pin might cause damage and malfunction.

3.1. Clear CMOS Jumper

The onboard CMOS RAM stores system configuration data and has an onboard battery power supply. To reset the CMOS settings, set the jumper on pins 2 and 3 while the system is off. Return the jumper to pins 1 and 2 afterwards. Setting the jumper while the system is on will damage the mainboard. The default setting is on pins 1 and 2.

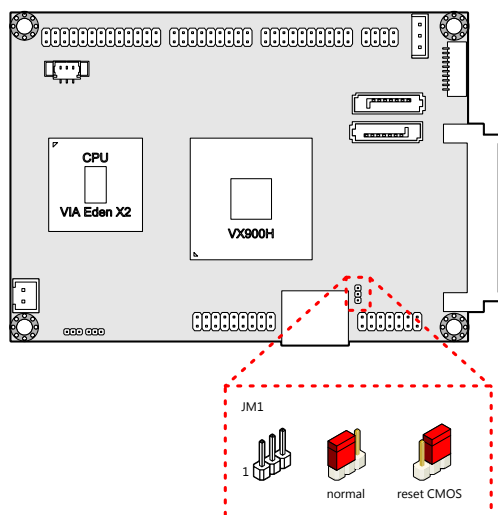


Figure 24: Clear CMOS jumper diagram

Setting	Pin 1	Pin 2	Pin 3
Normal(default)	Short	Short	Open
Clear CMOS	Open	Short	Short

Table 22: Clear CMOS jumper settings



Note:

Except when clearing the RTC RAM, never remove the cap from the Clear CMOS jumper default position. Removing the cap will cause system boot failure. Avoid clearing the CMOS while the system is on; it will damage the mainboard.

3.2. LVDS Panel Power Select Jumper

The mainboard has a jumper that controls the voltage delivered to the LVDS panel connector. The jumper is labeled as “PVDD1”. The jumper settings are shown below.

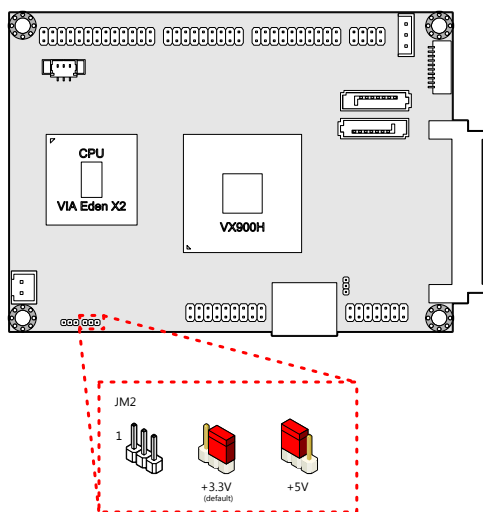


Figure 25: LVDS panel power select jumper diagram

Setting	Pin 1	Pin 2	Pin 3
+5V	Short	Short	Open
+3.3V (default)	Open	Short	Short

Table 23: LVDS panel power select jumper settings

3.3. Backlight Power Select Jumper

The mainboard has a jumper that controls the input voltage delivered to the LVDS inverter connector. The jumper is labeled as "IVDD_SEL1". The jumper settings are shown below.

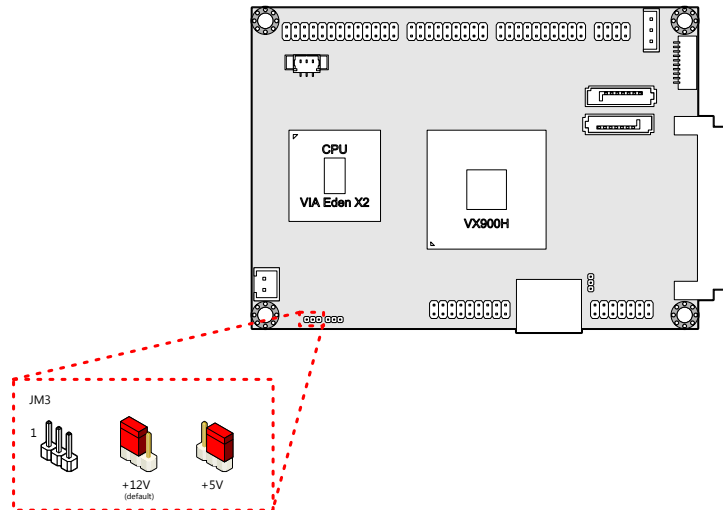


Figure 26: Backlight power select jumper diagram

Settings	Pin 1	Pin 2	Pin 3
+12V (default)	Short	Short	Open
+5V	Open	Short	Short

Table 24: Backlight power select jumper settings

4. Expansion Slots

4.1. PCIe and USB Combination Connector

The onboard PCIe and USB combination connector labeled “CN6” is for connecting directly to the P830-B expansion card. The connector pinout supports two miniPCIe x1 (supports USB 2.0).

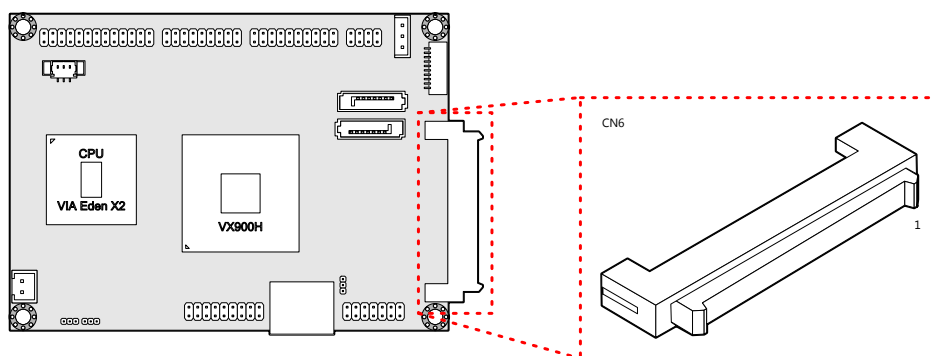


Figure 27: PCIe and USB combination connector diagram

Pin	Signal	Pin	Signal
1 - 25	Reserved	2 - 26	Reserved
27	GND	28	GND
29	+5V	30	-LID
31	+5V	32	NC
33	+5V	34	GND
35	+3.3V	36	+12V
37	+3.3V	38	+5VSUS
39	GND	40	+5VSUS
41	SMBDT	42	+5VSUS
43	SMBCK	44	-PEX3RST
45	-PEREQ1	46	-PEX2RST
47	-PEXWAKE	48	-PEX1RST
49	GND	50	GND
51	PE3CLK-	52	USBHP7-
53	PE3CLK+	54	USBHP7+
55	GND	56	GND
57	PETN9	58	PEXR9-
59	PETP9	60	PEXR9+
61	GND	62	GND
63	PETN8	64	PEXR8-
65	PETP8	66	PEXR8+
67	GND	68	GND
69	NC	70	PE2CLK-

71	NC	72	PE2CLK+
73	GND	74	GND
75	NC	76	NC
77	NC	78	NC
79	GND	80	GND

Table 25: PCIe and USB combination connector pinout

4.2. DDR3 SODIMM Memory Slot

The mainboard provide one 204-pin DDR3 SODIMM slot that supports non-ECC DDR3 800/1066 SODIMM memory modules. The memory slot can accommodate up to 4GB of DDR3 800/1066 memory. The memory slot is labeled as "SODIMM1". The location of the DDR3 memory slot is shown below.

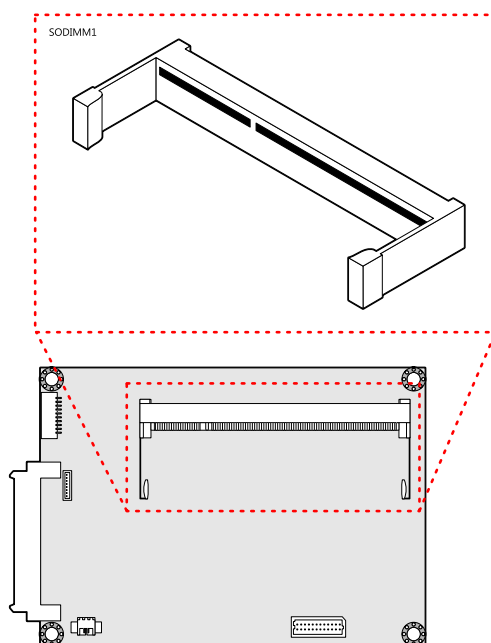


Figure 28: DDR3 SODIMM memory slot diagram

4.2.1. Installing a Memory Module

Step 1

Align the notch on the SODIMM memory module with the protruding wedge on the SODIMM memory slot. Insert the SODIMM memory module at a 30 degree angle relative to the SODIMM memory slot.

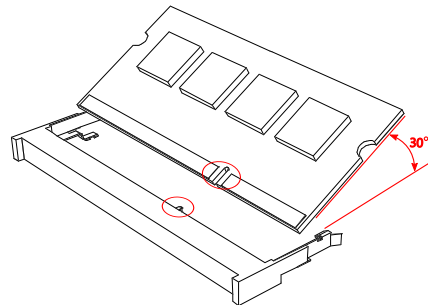


Figure 29: Inserting the memory module

Step 2

Insert the SODIMM memory module between the two rows of pins. Then push down until the locking clips lock the SODIMM memory module into place. There will be a slight tension as the SODIMM memory module is being locked.

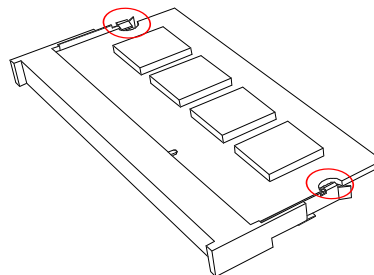


Figure 30: Locking the memory module

Step 3

Install the memory thermal pad on the top of the DRAM SODIMM memory module.

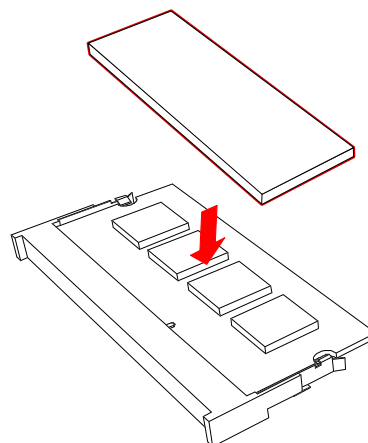


Figure 31: Installing memory thermal pad

The memory thermal pad is used for transferring the heat dissipation of memory to the thermal plate or bottom plate to attain memory cooling, and to ensure the operating temperature of the memory module should not exceed to 85°C. This helps to prevent damage of the memory module. The memory thermal pad to be used is requires a certain thickness in order to make contact with the memory thermal plate or bottom plate to excellently disperse the heat.

**Important:**

1. The customer/user should consider using the memory thermal pad and adding memory thermal plate or bottom plate on their chassis design.
2. The memory thermal plate/bottom plate material to be used should have an excellent thermal conductivity. Avoid using plastic or rubber materials.
3. The thickness of memory thermal pad should be based on customer's design. However, the minimum value of thermal conductivity K (W/m.k) is 1.5 and the maximum of hardness is 5 (Shore A).

4.2.2. Removing a Memory Module

Step 1

To disengage the locking clips, push the locking clips horizontally outward away from the SODIMM memory module.

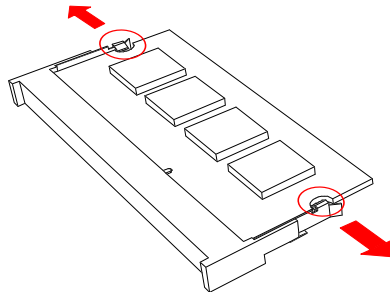


Figure 32: Disengaging the SODIMM locking clips

Step 2

When the locking clips have cleared, the SODIMM memory module will automatically pop up to the 30 degree angle. Remove the memory module.

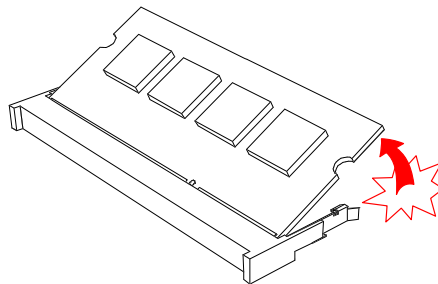


Figure 33: Removing the memory module

5. Hardware Installation

5.1. Removing and Installing the P830-A Companion Card

The VIA EPIA-P900 mainboard comes with pre-installed P830-A companion card. The pre-installed P830-A companion card is connected through VGA and USB combination pin header (VGA_USB1) and Gigabit Ethernet pin header (CN3).

5.1.1. Removing the P830-A

Step 1

Remove the two screws that secure the P830-A companion card. The screws are located at the top side of the companion card.

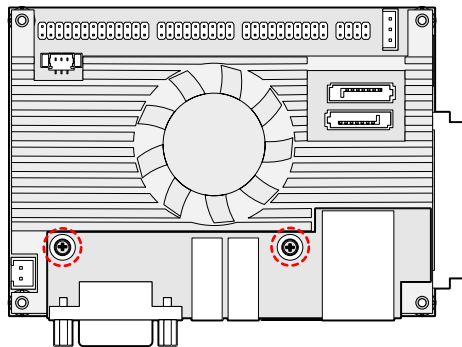


Figure 34: Unscrewing the P830-A companion card

Step 2

Gently pull up the P830-A companion card apart from the EPIA-900 mainboard. As you pull the companion card, keep the card evenly aligned to avoid bending the pin headers (VGA_USB1 and CN3) on the mainboard.

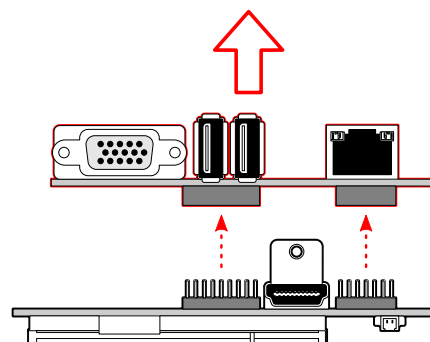


Figure 35: Pulling the P830-A companion card

5.1.2. Installing the P830-A

Step 1

Align the CON1 and CON2 board-to-board connectors on the P830-A with the VGA and USB combination pin header (VGA_USB1) block and Gigabit Ethernet pin header (CN3) block on the EPIA-P900 mainboard, respectively. In the figure below, the dotted rectangles represent the CON1 and CON2 connectors on the bottom side of the P830-A companion card

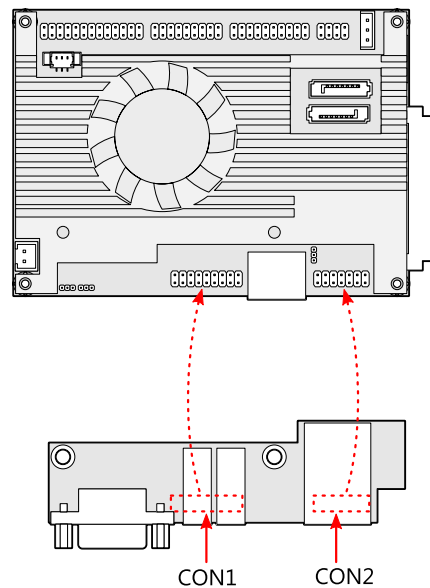


Figure 36: Aligning the P830-A companion card

Step 2

Then gently press down and apply even pressure until the pins on the EPIA-P900 mainboard have been fully inserted into the CON1 and CON2 board-to-board connectors of the P830-A companion card.

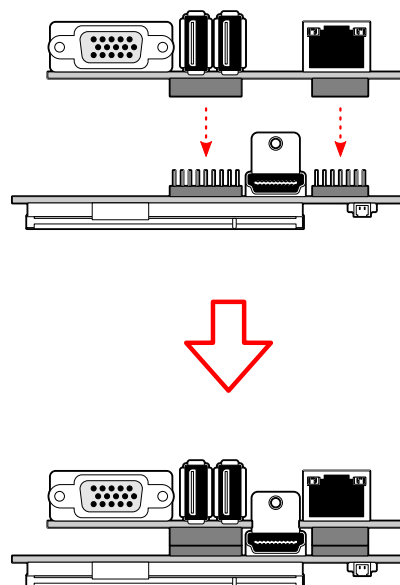


Figure 37: Connecting P830-A companion card

Step 3

Secure the EPIA-P830-A to the EPIA-P900 with two screws.

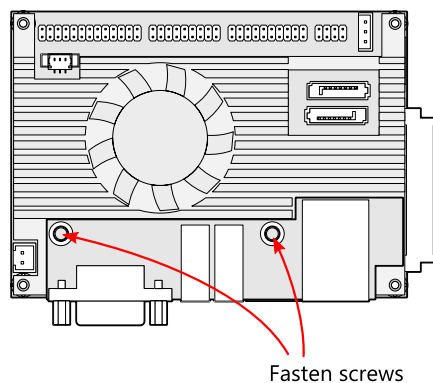


Figure 38: Securing the P830-A companion card

5.2. Installing the P830-B Expansion Card

Step 1

Align the PCIe and USB 2.0 combination connector (CN6) on the EPIA-P900 with the CN2 board-to-board female connector on the P830-B expansion card. Then gently insert the CN6 connector into the CN2 connector until the CN6 connector is fully inserted.

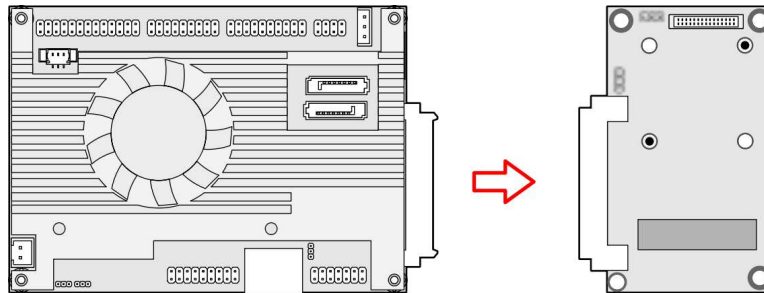


Figure 39: Installing the P830-B expansion card



Note:

The P830-B expansion card is for project based enquiries only. Please contact sales for detailed information.

5.3. Installing into a Chassis

The EPIA-P900 can be fitted into any chassis that has the mounting holes for compatible with the standard Pico-ITX mounting hole locations. Additionally, the chassis must meet the minimum height requirements for specified areas of the mainboard. If an expansion card (P830-B) is being used, the chassis will need to accommodate the additional space requirements.

5.3.1. Suggested minimum chassis dimensions

The figure below shows the suggested minimum space requirements that a chassis should have in order to work well with the EPIA-P900.

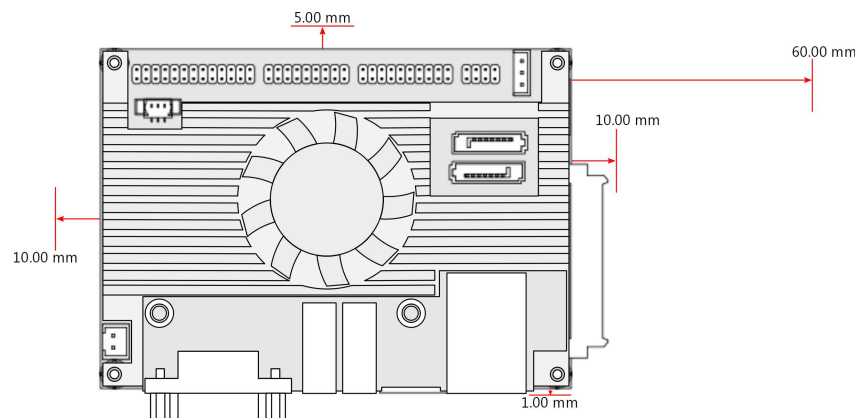


Figure 40: Suggested minimum chassis dimensions

Each side of the mainboard should have a buffer zone from the internal wall of the chassis. The side of the mainboard that accommodates the I/O coastline should have a buffer of 1.00mm. The side on the opposite end of the I/O coastline should have a buffer of at least 5.00mm. The two sides adjacent to the I/O coastline should have at least a 10.00mm buffer.

For the side that is close to the PCIe and USB expansion connector (CN6), the buffer should be at least 60.00mm if the P830-B expansion card will be used.

5.3.2. Suggested minimum chassis height

The figure below shows the suggested minimum height requirements for the internal space of the chassis. It is not necessary for the internal ceiling to be evenly flat. What is required is that the internal ceiling height must be strictly observed for each section that is highlighted.

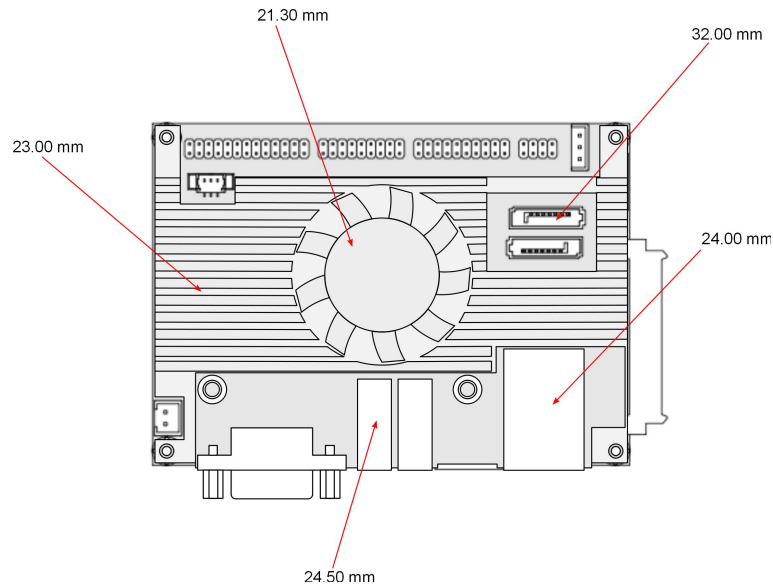


Figure 41: Suggested minimum internal chassis ceiling height



Note:

In getting the minimum height requirements for internal space of the chassis, it is required to consider the heights of the connectors (such as SPI connector, LVDS panel connector and DDR3 SODIMM slot) on the bottom side of the EPIA-P900 mainboard.

5.3.3. Suggested keepout areas

The figure below shows the areas of the mainboard that is highly suggested to leave unobstructed.

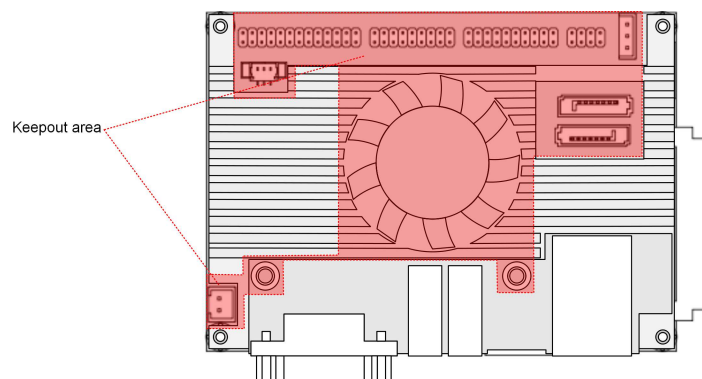


Figure 42: Suggested keepout areas

6. BIOS Setup Utility

6.1. Entering the BIOS Setup Utility

Power on the computer and press **Delete** during the beginning of the boot sequence to enter the BIOS Setup Utility. If the entry point has passed, restart the system and try again.

6.2. Control Keys

Up	Move up one row
Down	Move down one row
Left	Move to the left in the navigation bar
Right	Move to the right in the navigation bar
Enter	Access the highlighted item / Select the item
Esc	Jumps to the Exit screen or returns to the previous screen
+¹	Increase the numeric value
-¹	Decrease the numeric value
F1	General help ²
F7	Discard Changes
F9	Load optimized defaults
F10	Save all the changes and exit

Note:

1. Must be pressed using the 10-key pad.
2. The General help contents are only for the Status Page and Option Page setup menus.

6.3. Getting Help

The BIOS Setup Utility provides a “**General Help**” screen. This screen can be accessed at any time by pressing **F1**. The help screen displays the keys for using and navigating the BIOS Setup Utility. Press **Esc** to exit the help screen.

6.4. System Overview

The System Overview screen is the default screen that is shown when the BIOS Setup Utility is launched. This screen can be accessed by traversing the navigation bar to the “Main” label.

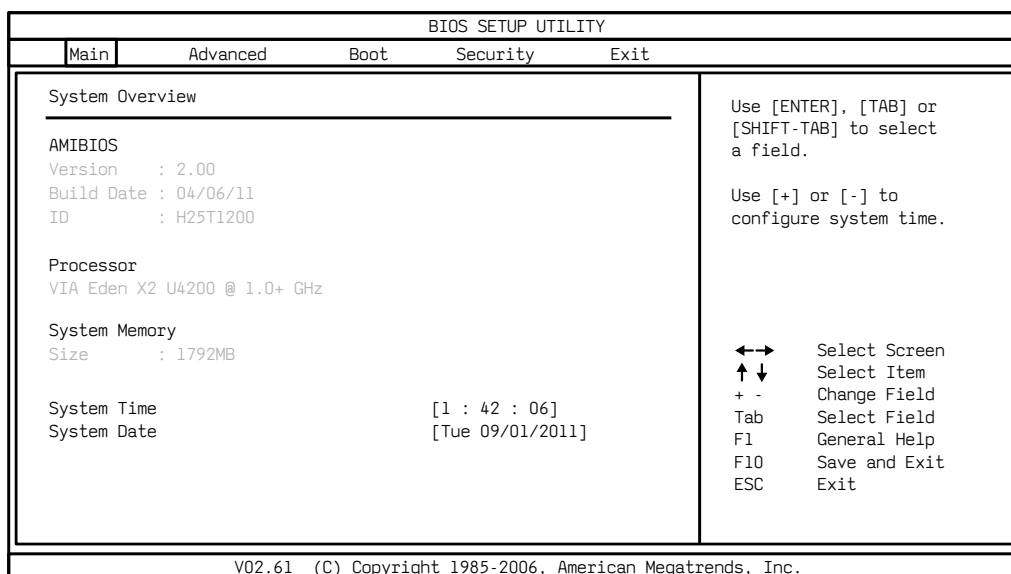


Figure 43: Illustration of the Main menu screen

6.4.1. AMIBIOS

The content in this section of the screen shows the current BIOS version, build date, and ID number.

6.4.2. Processor

This content in this section shows the CPU information that has been detected. This information includes the CPU name and speed.

6.4.3. System Memory

This section shows the amount of available memory that has been detected.

6.4.4. System Time

This section shows the current system time. Press **Tab** to traverse right and **Shift+Tab** to traverse left through the hour, minute, and second segments. The **+** and **-** keys on the number pad can be used to change the values. The time format is [Hour : Minute : Second].

6.4.5. System Date

This section shows the current system date. Press **Tab** to traverse right and **Shift+Tab** to traverse left through the month, day, and year segments. The **+** and **-** keys on the number pad can be used to change the values. The weekday name is automatically updated when the date is altered. The date format is [Weekday, Month, Day, Year].

6.5. Advanced Settings

The Advanced Settings screen shows a list of categories that can provide access to a sub-screen. Sub-screen links can be identified by the preceding right-facing arrowhead.

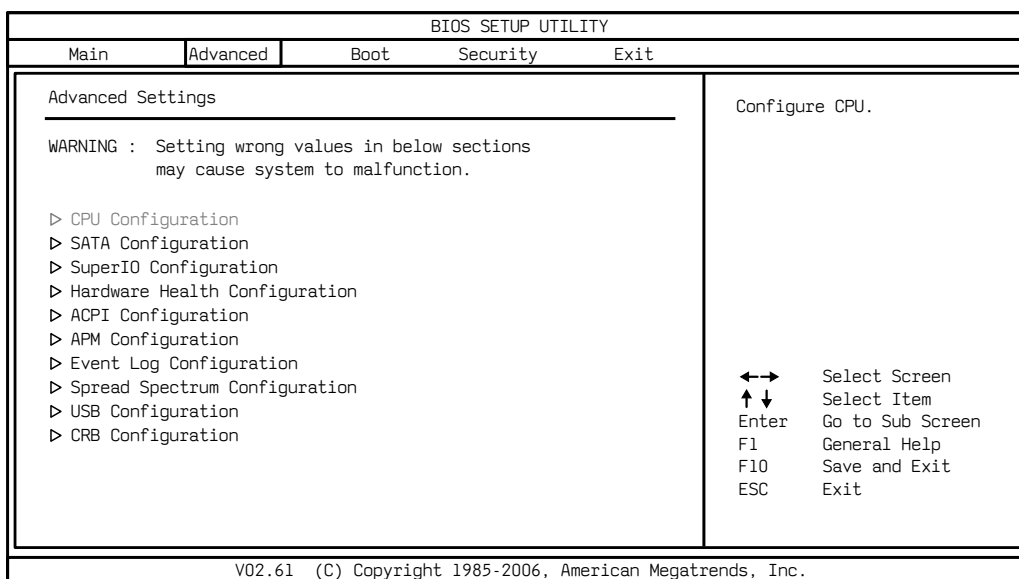


Figure 44: Illustration of the Advanced Settings screen

The Advanced Settings screen contains the following links:

- CPU Configuration
- SATA Configuration
- SuperIO Configuration
- Hardware Health Configuration
- ACPI Configuration
- APM Configuration
- Event Log Configuration
- Spread Spectrum Configuration
- USB Configuration
- CRB Configuration

6.5.1. CPU Configuration

The CPU Configuration screen shows detailed information about the built-in processor. In addition to the processor information, the thermal controls can be set.

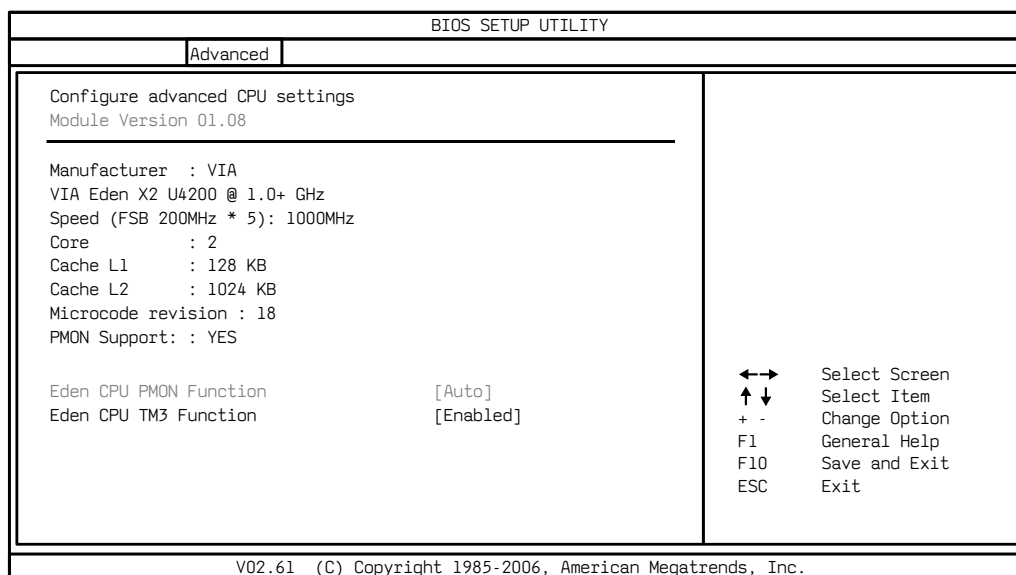


Figure 45: Illustration of the CPU Configuration screen

6.5.1.1. Eden CPU PMON Function

The Eden CPU PMON Function has two settings: Auto and Disabled. When set to “Auto”, the PMON function will be enabled and controlled the CPU speed to perform automatically at best performance to comply with the given system applications.



Note:

The VIA Eden® X2 1.0+ GHz highest speed is 1.2GHz with PMON function enabled. Even the CPU is running at default speed, the system (Windows/Linux) and the BIOS will always display the highest speed.

6.5.1.2. Eden CPU TM3 Function

The Eden CPU TM3 Function has two settings: Disabled and Enabled. When the setting is changed to “Disabled”, the CPU’s built-in thermal sensor will not function. When the setting is changed to “Enabled”, the thermal sensor will automatically adjust the CPU ratio and V_{CORE} to prevent the CPU from overheating.

6.5.2. SATA Configuration

The SATA Configuration screen shows links to the primary and secondary SATA hard drive information screens.

BIOS SETUP UTILITY	
Advanced	
SATA Configuration	
Serial ATA IDE devices	
▷ SATA-1 Primary IDE	: [Hard Disk]
▷ SATA-1 Secondary IDE	: [Not Detected]
<p>While entering setup, BIOS auto detects the presence of SATA devices. This displays the status of auto detection of SATA devices.</p> <p> ↔ Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit </p>	
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 46: Illustration of SATA Configuration screen

6.5.2.1. Hard Disk Information

When a hard drive is detected, the hard drive's detailed information can be displayed on the SATA-1 Primary/Secondary IDE sub-screen.

BIOS SETUP UTILITY	
Advanced	
SATA-1 Primary IDE	
Device	: Not Detected
PIO Mode	: Auto
DMA Mode	: Auto
<p>Select PIO Mode.</p> <p> ↔ Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit </p>	
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 47: Illustration of SATA-1 Primary IDE screen

In addition, the PIO and DMA modes may be configured for each SATA hard drive.

6.5.2.1.1. PIO Mode

The PIO Mode has six possible settings: Auto, 0, 1, 2, 3, and 4. The "Auto" setting enables the BIOS to autonomously determine the appropriate PIO mode for the hard drive. If a manual setting is preferred, then be sure the correct PIO mode of the hard drive is used. It is not recommended to set the PIO mode higher than what the hard drive manufacturer states.

6.5.2.1.2. DMA Mode

The DMA Mode has four possible settings: Auto, SWDMAn, MWDMAn and UDMAn. If a manual setting is preferred, then be sure the correct DMA mode of the hard drive is used.

Auto

The "Auto" setting enables the BIOS to automatically detect DMA mode.

SWDMAn

Single Word DMA mode.

MWDMAn

Multi Word DMA mode.

UDMAn

Ultra Word DMA mode.

6.5.3. SuperIO Configuration

The SuperIO Configuration screen shows the specific addresses and IRQs of the onboard serial ports.

BIOS SETUP UTILITY	
Advanced	
Configure F81865F Super IO Chipset	
Serial Port1 Address	[3F8/IRQ4]
Serial Port2 Address	[2F8/IRQ3]
<p>Allows BIOS to select Serial Port1 base addresses.</p> <p> ←→ Select Screen ↑↓ Select Item + - Change Option F1 General Help F10 Save and Exit ESC Exit </p>	
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 48: Illustration of SuperIO Configuration screen

6.5.3.1. Serial Ports 1 to 2 Address

This option allows the user to select the Serial Port 1 and 2 base I/O address and interrupt request address. The Serial Port 1 to 2 has four selectable options.

Port	Address and IRQs
1	Disabled, 3F8/IRQ4, 3E8/IRQ4, 2E8/IRQ3
2	Disabled, 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3

Table 26: Serial port addresses and IRQs

6.5.4. Hardware Health Configuration

The Hardware Health Configuration screen has no editable fields. The system temperature is taken from an optional sensor that is connected to the J5 pin header.

BIOS SETUP UTILITY	
Advanced	
Hardware Health Configuration	
CPU Temperature	:33°C/91°F
System Temperature	:28°C/82°F
Fan1 Speed	:3000 RPM
+1.2V (VIN1)	:1.240 V
+3.3V (VIN2)	:3.352 V
Smart FAN 1	: [Auto]
Fan configuration mode setting ←→ Select Screen ↑↓ Select Item + - Change Option F1 General Help F10 Save and Exit ESC Exit	
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 49: Illustration of Hardware Health Configuration screen

6.5.4.1. Smart FAN 1

The Smart FAN features have two options: Auto and Full Speed. The “Auto” option enables the BIOS to adjust the fan speed according to the needs of the CPU and system. The “Full Speed” option forces the fans to run at their maximum RPM.

6.5.5. ACPI Configuration

Advanced Configuration and Power Interface (ACPI) grants the operating system direct control over system power management. The ACPI Configuration screen can be used to set a number of power management related functions.

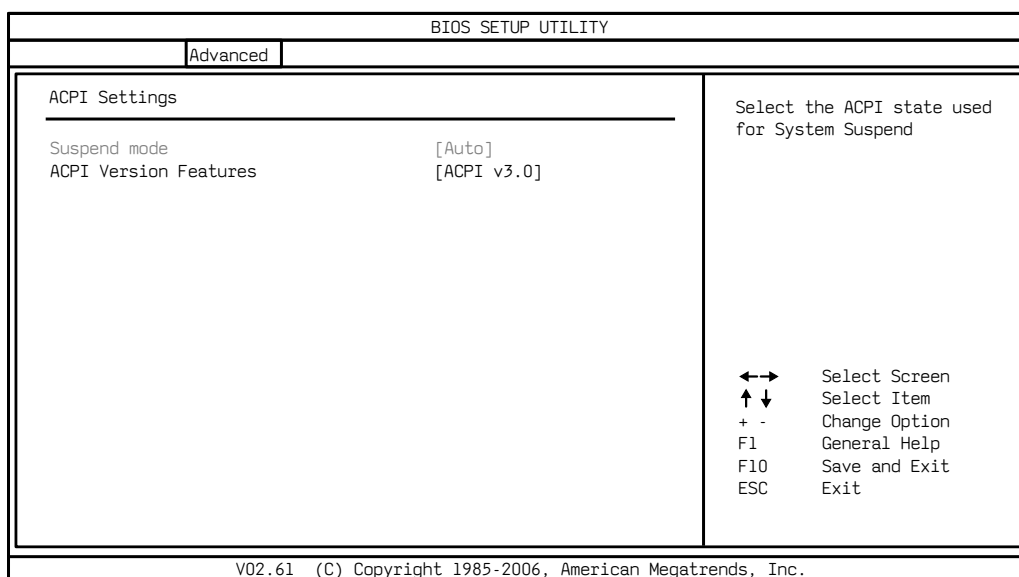


Figure 50: Illustration of ACPI Configuration screen

6.5.5.1. Suspend Mode

The Suspend Mode field has three selectable options.

S1 (POS)

S1/Power On Suspend (POS) is a low power state. In this state, no system context (CPU or chipset) is lost and hardware maintains all system contexts.

S3 (STR)

S3/Suspend To RAM (STR) is a power-down state. In this state, power is supplied only to essential components such as main memory and wakeup-capable devices. The system context is saved to main memory, and context is restored from the memory when a "wakeup" event occurs.

Auto

When the Suspend Mode is set to Auto, the operating system will control the power state.

6.5.5.2. ACPI Version Features

The ACPI Version Features enables the BIOS to support the designated ACPI specification. There are three versions to choose from: ACPI 1.0, ACPI 2.0, and ACPI 3.0.

6.5.6. APM Configuration

Advanced Power Management (APM) enables the operating system to co-work with the BIOS to control the system power management. The APM Configuration screen can be used to set a number of power management functions.

BIOS SETUP UTILITY	
Advanced	
Power Button Mode	[On/Off]
Restore on AC/Power Loss	[Last State]
Advanced Resume Event Controls	
Resume On LAN	[Disabled]
Resume On KBC	[Disabled]
Wake-Up Key	[Any Key]
Resume On Mouse	[Disabled]
Resume On RTC Alarm	[Disabled]
<div>Options</div> <div> On/Off Standby Suspend </div> <div> ↔ Select Screen ↑↓ Select Item + - Change Option F1 General Help F10 Save and Exit ESC Exit </div>	
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 51: Illustration of APM Configuration screen

6.5.6.1. Power Button Mode

The Power Button Mode has three options.

On/Off

When On/Off is selected, pressing the power button will instantly cause the system to power on or off.

Standby

When Standby is selected; the power button must be pressed and held down for 4 seconds before the system will power off.

Suspend

When Suspend is selected, pressing the power button will instantly cause the system to enter suspend mode.

6.5.6.2. Restore on AC/Power Loss

Restore on AC/Power Loss defines how the system will respond after AC power has been interrupted while the system is on. There are three options.

Power Off

The Power Off option keeps the system in an off state until the power button is pressed again.

Power On

The Power On option restarts the system when the power has returned.

Last State

The Last State option restores the system to its previous state when the power was interrupted.

6.5.6.3. Resume on LAN

Resume on LAN allows to turned on a system that has been put into suspend or standby mode. When this feature is enabled, LAN activity as defined in the **Resume on LAN** feature will cause the system to wake up. This feature has three options.

S1

The S1 option enables LAN activity to be detected if the system is in S1 power saving mode.

S1/S3/S4/S5

The S1/S3/S4/S5 option enables LAN activity to be detected if the system is in S1/S3/S4/S5 power saving mode.

Disabled

The Disabled option disables the detection of all LAN activity.

6.5.6.4. Resume on KBC

Resume on KBC wakes up a system that has been put into suspend or standby mode. When this feature is enabled, keyboard activity as defined in the **Wake-Up Key** feature will cause the system to wake up. This feature has three options.

S3

The S3 option enables keyboard activity to be detected if the system is in S3 power saving mode.

S3/S4/S5

The S3/S4/S5 option enables keyboard activity to be detected if the system is in S3/S4/S5 power saving mode.

Disabled

The Disabled option disables the detection of all keyboard activity.

6.5.6.5. Wake-Up Key

The Wake-Up Key feature can only be set when **Resume on KBC** is set to "S3" or "S3/S4/S5". Otherwise, this feature will be not selectable. This feature has two options.

Any Key

The Any Key option enables any key on the keyboard to trigger the Wake-Up event.

Specific Key

The Specific Key option unlocks the **Wake-Up Password** feature.

6.5.6.6. Wake-Up Password

The Wake-Up Password feature can only be set when the **Wake-Up Key** feature is set to "Specific Key". This feature enables the user to specify a key sequence that must be entered in order to wake up the system.

The key sequence can consist of up to 6 alphanumeric characters and some special characters. Function keys and modifier keys (such as Ctrl, Alt, Del, etc.) cannot be used.

6.5.6.7. Resume on Mouse

Resume on Mouse wakes up a system that has been put into suspend or standby mode. When this feature is enabled, any mouse activity that is detected will cause the system to wake up. This feature has three options.

S3

The S3 option enables any mouse activity to be detected if the system is in S3 power saving mode.

S3/S4/S5

The S3/S4/S5 option enables any mouse activity to be detected if the system is in S3/S4/S5 power saving mode.

Disabled

The Disabled option disables the detection of all mouse activity.

6.5.6.8. Resume on RTC Alarm

Resume on RTC Alarm can only be used if **Resume on Software RTC Alarm** is not enabled. This feature enables the BIOS to automatically power on the system at a scheduled time. When enabled, the **RTC Alarm Date** and **System Time** features will be unlocked.

6.5.6.9. RTC Alarm Date (Days)

The RTC Alarm Date feature is visible only when **Resume on RTC Alarm** is enabled. This feature enables the user to specify a specific date each month or daily recurrence. Use the **+** and **-** keys on the number pad to change the value of the RTC Alarm Date.

Every Day

The Every Day option triggers the RTC Alarm daily.

1 – 31

When a specific numeric date is selected, the RTC Alarm will be triggered on that day of the month.

6.5.6.10. System Time

The System Time option enables the user to specify the time the system should power on for the date that is set in **RTC Alarm Date**.

6.5.7. Event Log Configuration

The Event Logging Configuration screen displays three features for accessing, modifying, and deleting event logs.

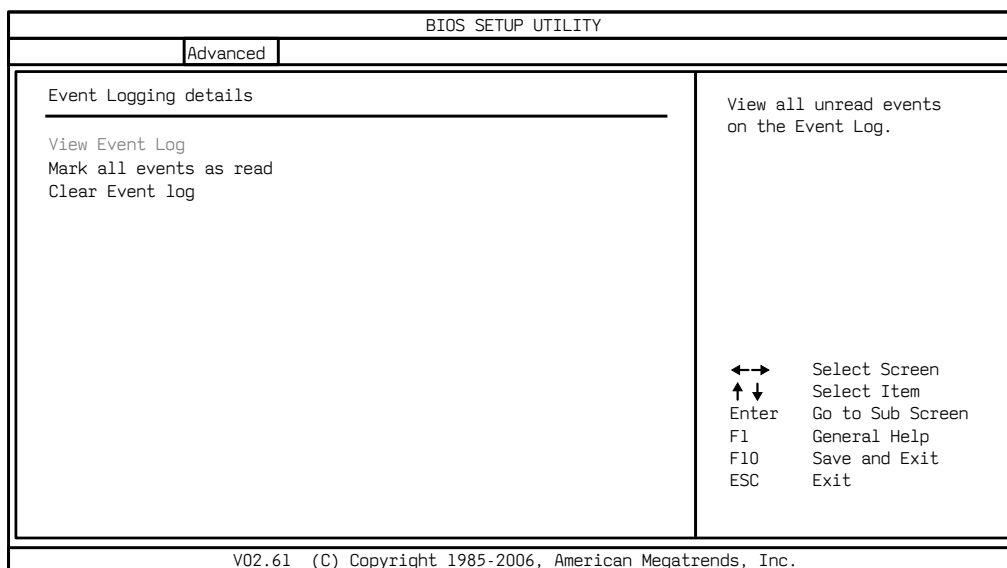


Figure 52: Illustration of Event Log Configuration screen

6.5.7.1. View Event Log

The View Event Log feature displays all event logs that have been recorded. Event logs include information such as keyboard errors, etc.

6.5.7.2. Mark all events as read

The Mark all events as read feature provides a quick mechanism to flag all event logs as having been accessed.

6.5.7.3. Clear Event log

The Clear Event log feature deletes all event logs from the BIOS memory.

6.5.8. Spread Spectrum Configuration

The Spread Spectrum Configuration screen enables access to the Spread Spectrum Setting feature.

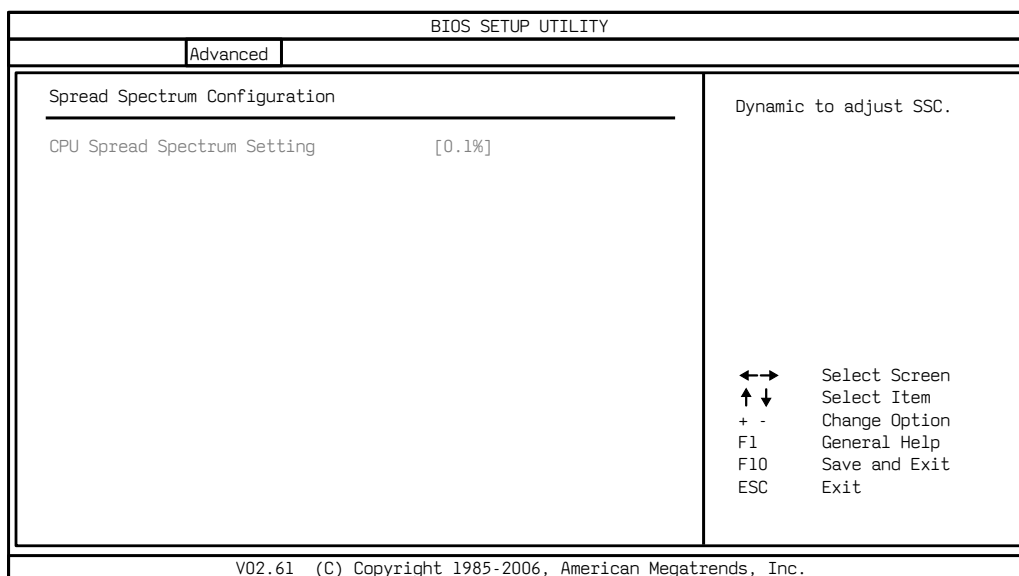


Figure 53: Illustration of Spread Spectrum Configuration screen

6.5.8.1. CPU Spread Spectrum Setting

The Spread Spectrum Setting feature enables the BIOS to modulate the clock frequencies originating from the mainboard. The settings are in percentages of modulation. Higher percentages result in greater modulation of clock frequencies. This feature has settings that range from 0.1% to 0.9%.

6.5.9. USB Configuration

The USB Configuration screen shows the number of connected USB devices. Additionally, support for various USB features can be enabled or disabled.

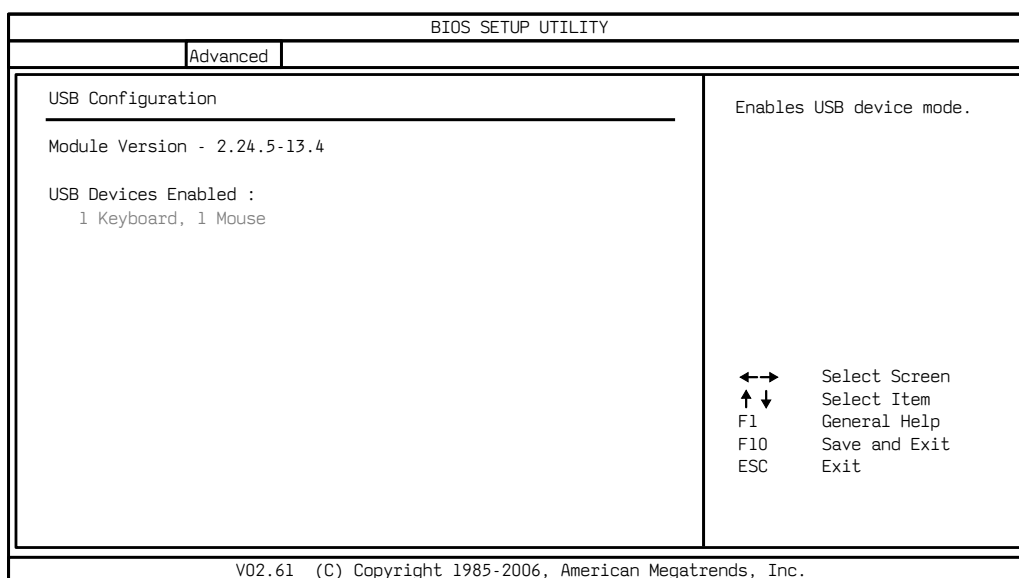


Figure 54: Illustration of USB Configuration screen

6.5.10. CRB Configuration

The CRB Configuration screen includes several chipset settings.

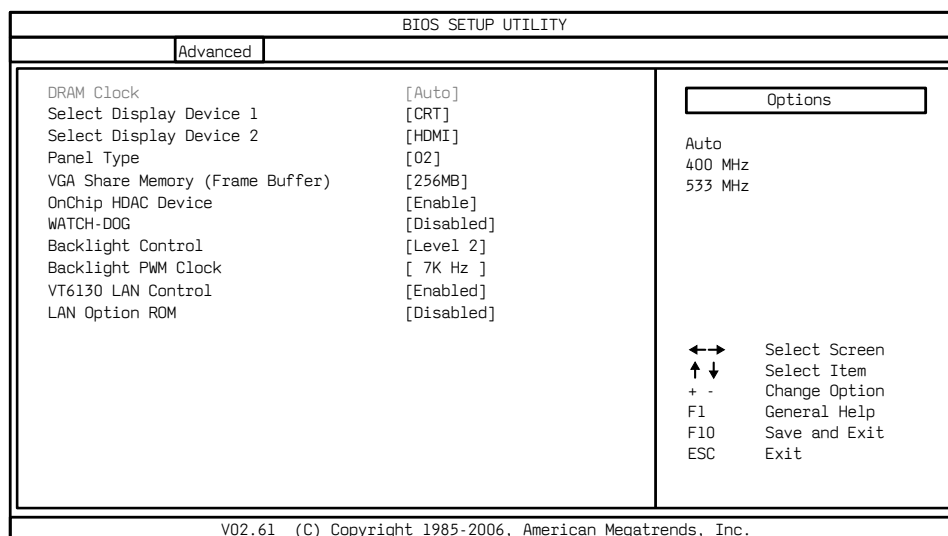


Figure 55: Illustration of CRB Configuration screen

6.5.10.1. DRAM Clock

The DRAM Clock feature enables the user to determine how the BIOS handles the memory clock frequency. The memory clock can either be dynamic or static. This feature has three options.

Auto

The Auto option enables the BIOS to select a compatible clock frequency for the installed memory.

400 MHz

The 400 MHz option forces the BIOS to be fixed at 800 MHz for DDR3 memory modules.

533 MHz

The 533 MHz option forces the BIOS to be fixed at 1066 MHz for DDR3 memory modules.

6.5.10.2. Select Display Device 1 and 2

The Select Display Device feature enables the user to choose a specific display interface. This feature has twelve options: CRT, LCD and HDMI®.

6.5.10.3. Panel Type

This feature enables the user to specify the resolution of the display being used with the system. The panel types are predefined in the VGA VBIOS.

Panel Type	Resolution	Panel Type	Resolution
00	640 × 480	08	800 × 480
01	800 × 600	09	1024 × 600
02	1024 × 768	10	1366 × 768
03	1280 × 768	11	1600 × 1200
04	1280 × 1024	12	1680 × 1050
05	1400 × 1050	13	User define
06	1440 × 900	14	User define
07	1280 × 800	15	User define

Table 27: Panel types resolution

6.5.10.4. VGA Share Memory (Frame Buffer)

The VGA Share Memory feature enables the user to choose the amount of the system memory to reserve for use by the integrated graphics controller. The amount of memory that can be reserved ranges from 64 – 512 MB.

6.5.10.5. OnChip HDAC Device

The OnChip HDAC Device feature enables the BIOS to control the high definition audio codec in the chipset. This feature has two options: enable and disable.

6.5.10.6. WATCH-DOG

The WATCHDOG Timer Enable feature unlocks two other features that enable the BIOS to monitor the state of the system. This feature has two options: enabled or disabled.

6.5.10.7. Unit-Select

The Unit-Select feature is only available if the **WATCH-DOG** feature has been enabled. This feature has two options: minutes and seconds.

6.5.10.8. Time-Select

The Time-Select is only available if the **WATCH-DOG** feature has been enabled. This feature requires the user to input an integer in the range of 0–255 if the **Unit-Select** feature is set to “Seconds”. If the **Unit-Select** feature is set to “Minutes”, then the user can only input an integer in the range of 0–17. If the user inputs “0”, the **WATCH-DOG** feature will be disabled regardless of the setting for **Unit-Select**.

6.5.10.9. Backlight Control

The Backlight Control feature enables the user to control the brightness of the LCD backlight. This feature has four options.

Level 1 25% Light

Level 2 50% Light

Level 3 75% Light

Level 4 100% Light

6.5.10.10. Backlight PWM Clock

The Backlight PWM Clock feature enables the user to correct the LCD backlight PWM clock. This feature has four options: 14 KHz, 7 KHz, 110 Hz, 54.4 Hz.

6.5.10.11. VT6130 LAN Control

The VT6130 LAN Control feature determines whether the onboard LAN controller will be used or not.

6.5.10.12. LAN Option ROM

The LAN Option ROM feature will only be visible if the **VT6130 LAN Control** feature is enabled. If the LAN Option ROM feature is enabled, then the system will load a separate ROM for the LAN controller in order to boot through the Gigabit Ethernet.

6.6. Boot Settings

The Boot Settings screen has a single link that goes to the **Boot Settings Configuration** and **Boot Device Priority** screens.

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Exit
Boot Settings <hr/> ▷ Boot Settings Configuration ▷ Boot Device Priority		Configure Settings during System Boot. ←→ Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.				

Figure 56: Illustration of Boot Settings screen

6.6.1. Boot Settings Configuration

The Boot Settings Configuration screen has several features that can be run during the system boot sequence.

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Exit
Boot Settings Configuration <hr/> Quick Boot [Enabled] Quiet Boot [Enabled] Bootup Num-Lock [On] Wait for 'F1' if Error [Enabled] Hit 'DEL' Message Display [Enabled]		Allows BIOS to skip certain tests while booting. This will decrease the time needed to boot the system. ←→ Select Screen ↑↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.				

Figure 57: Illustration of Boot Settings Configuration

6.6.1.1. Quick Boot

The Quick Boot feature enables the BIOS to skip certain tests in order to speed up the boot sequence. This feature has two options: enabled and disabled.

6.6.1.2. Quiet Boot

The Quiet Boot feature hides all of the Power-on Self Test (POST) messages during the boot sequence. Instead of the POST messages, the user will see an OEM logo. This feature has two options: enabled and disabled.

6.6.1.3. Bootup Num-Lock

The Bootup Num-Lock feature determines how the 10-key pad will behave. When the feature is enabled, the 10-key pad will behave as a number pad. When the feature is disabled, the 10-key pad will behave as cursor navigation keys.

6.6.1.4. Wait for 'F1' if Error

This feature determines how the system will respond if an error is detected during the boot sequence. If this feature is enabled, the BIOS will pause booting and wait for the user to press F1 to enter the BIOS setup menu. This feature has two options: enabled and disabled.

6.6.1.5. Hit 'DEL' Message Display

This feature determines if the BIOS will display a POST message that informs the user how to access the BIOS Setup Utility. This feature has two options: enabled and disabled.



Note:

If the Quiet Boot option is enabled, the settings of this feature will have no effect.

6.6.2. Boot Device Priority

The Boot Device Priority screen lists all bootable devices.

BIOS SETUP UTILITY	
Boot	
<p>Boot Device Priority</p> <hr/> <p>1st Boot Device [Network: VIA Networking]</p>	<p>Specifies the best sequence from the available devices.</p> <p>A device enclosed in parenthesis has been disabled in the corresponding type menu.</p> <p> ←→ Select Screen ↑↓ Select Item + - Change Option F1 General Help F10 Save and Exit ESC Exit </p>
V02.61 (C) Copyright 1985-2006, American Megatrends, Inc.	

Figure 58: Illustration of Boot Device Priority

6.6.2.1. 1st Boot Device

This feature specifies the boot sequence from the available devices. The available boot devices are detected dynamically and bootable devices will be listed accordingly. This feature has two options: Network: VIA Networking Bootagent, and Disabled]

6.7. Security Settings

The Security Settings screen provides a way to restrict access to the BIOS or even the entire system.

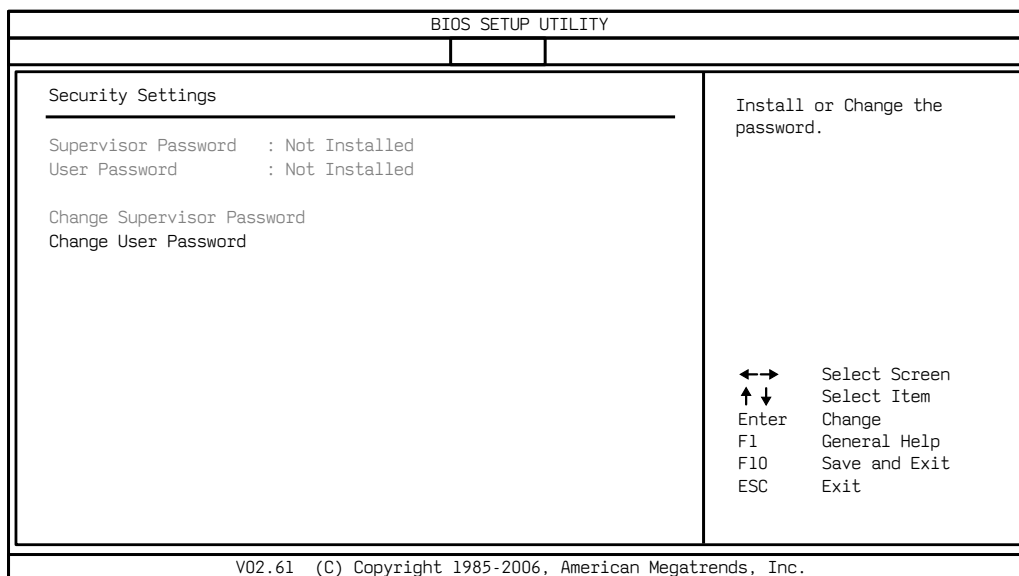


Figure 59: Illustration of Security Settings screen

6.7.1. Change Supervisor Password

This option is for setting a password for accessing the BIOS setup utility. When a password has been set, a password prompt will be displayed whenever the BIOS setup utility is launched. This prevents an unauthorized person from changing any part of the system configuration.

When a supervisor password is set, the **Password Check** option will be unlocked.

6.7.2. Change User Password

This option is for setting a password for non-supervisors. When a user password is set, the Clear User Password and Password Check options will be unlocked.

6.7.3. Clear User Password

This option is only available when the user accesses the BIOS Setup Utility when the user password has been specified.

6.7.4. Password Check

This feature is compulsory when the **Change Supervisor Password** option is set. The user will have up to three chances to enter the correct password before the BIOS forces the system to stop booting. If the user does not enter the correct password, the keyboard will also lock up. The only way to get past this is to do a hard reboot (i.e., use the system reset button or cut off the power to the system). A soft reboot (i.e., Ctrl+Alt+Del) will not work because the keyboard will be locked. This feature has two options.

Setup

The Setup option forces user to enter a password in order to access the BIOS Setup Utility.

Always

The Always option forces users to enter a password in order to boot up the system.

6.8. Exit Options

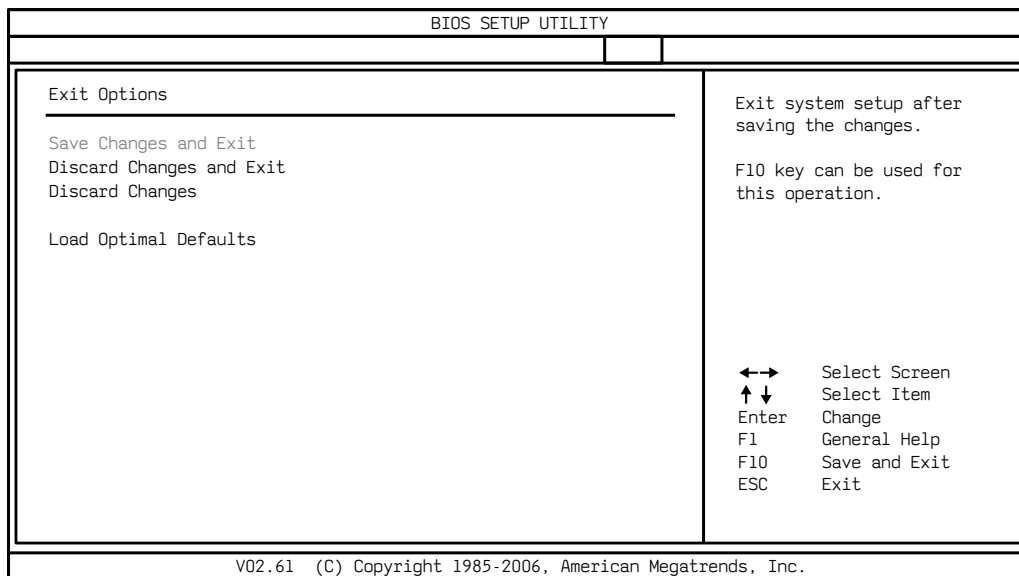


Figure 60: Illustration of Exit Options screen

6.8.1. Save Changes and Exit

Save all changes to the BIOS and exit the BIOS Setup Utility. The “F10” hotkey can also be used to trigger this command.

6.8.2. Discard Changes and Exit

Exit the BIOS Setup Utility without saving any changes. The “Esc” hotkey can also be used to trigger this command.

6.8.3. Discard Changes

This command reverts all changes to the settings that were in place when the BIOS Setup Utility was launched.

6.8.4. Load Optimal Defaults

Load optimal default values for all the setup items. The default optimized values are defined by the mainboard manufacturer to provide optimized environment for a basic system.

7. Software and Technical Support

7.1. Microsoft and Linux Support

The EPIA-P900 mainboard is highly compatible with Microsoft Windows and Linux operating systems.

7.1.1. Microsoft Driver Support

- The EPIA-P900 mainboard is compatible with Microsoft operating systems. The latest Windows drivers can be downloaded from the VIA website at <http://www.viatech.com>.
- For embedded operating systems, the related drivers can be found in the VIA website at <http://www.viatech.com>.

7.1.2. Linux Driver Support

The EPIA-P900 mainboard is highly compatible with many Linux distributions.

- Support and drivers are provided through various methods including: Drivers provided by VIA
- Using a driver built into a distribution package
- Visiting <http://www.viatech.com> for the latest updated drivers
- Installing a third party driver (such as the ALSA driver from the Advanced Linux Sound Architecture project for integrated audio)

7.2. Technical Supports and Assistance

- For utilities downloads, latest documentation and new information about the EPIA-P900, go to <http://www.viatech.com/en/boards/pico-itx/epia-p900/>
- For technical support and additional assistance, always contact your local sales representative or board distributor, or go to <http://www.viatech.com/en/contact.jsp> to fill up the form request.
- For OEM clients and system integrators developing a product for long term production, other code and resources may also be made available. Contact VIA to submit a request.

Appendix A. Installing Wireless Accessories

The section provides the installation of the optional wireless accessories for the EPIA-P900 mainboard.

A.1. Installing the VNT9271 USB Wi-Fi Dongle

Step 1

Locate a USB 2.0 port on the back panel I/O.

Step 2

Insert the VNT9271 USB Wi-Fi dongle in one of the USB 2.0 port.

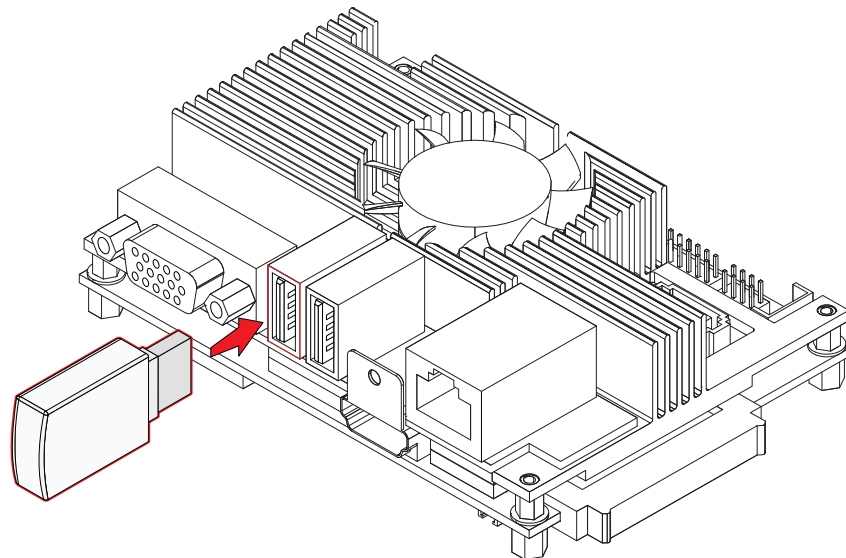


Figure 61: Inserting the VNT9271 USB Wi-Fi module

A.2. Installing the EMIO-1533 USB Wi-Fi Module

Step 1

Mount the EMIO-1533 to the prepared standoff in the chassis. Align the two mounting holes on the EMIO-1533 module with the mounting holes on the standoffs. And then secure the EMIO-1533 module in place with two screws.

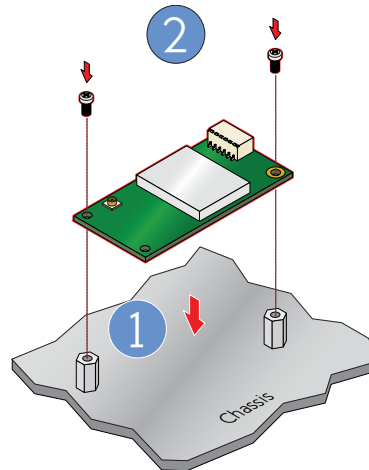


Figure 62: Installing EMIO-1533 USB Wi-Fi module

Step 2

Connect one end of USB Wi-Fi cable to pin 4, 6, 8 and 10 of USB and USB Device Combination pin header (CN2) on EPIA-P900 board, and then connect the other end of the cable to the EMIO-1533 module.

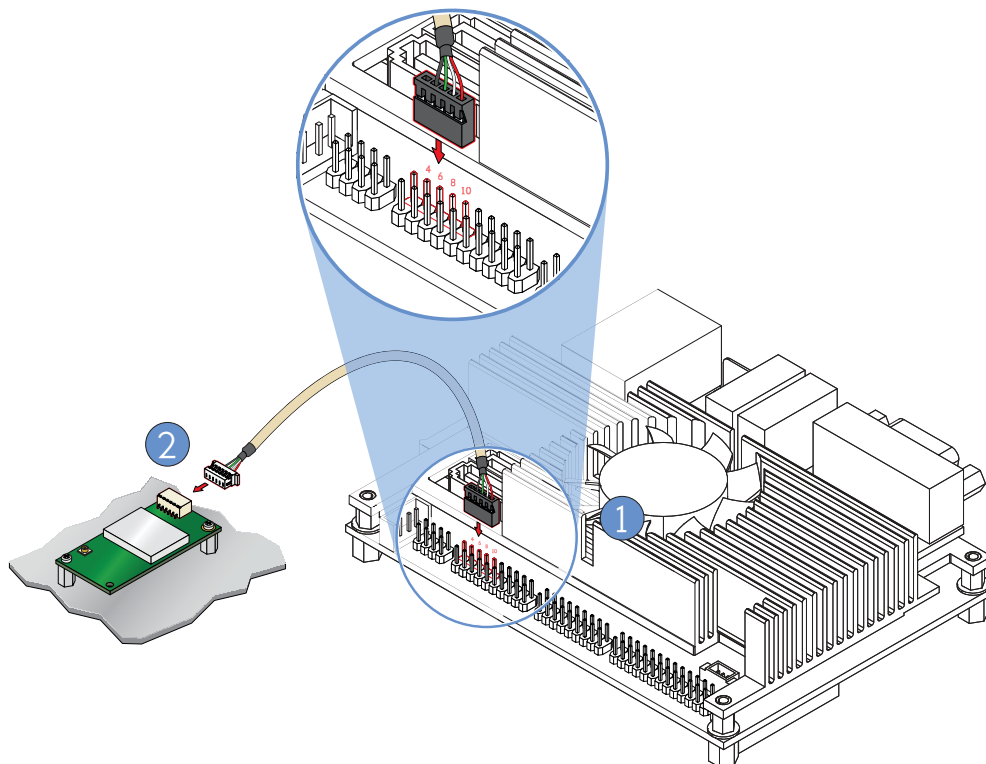


Figure 63: Connecting the USB Wi-Fi cable diagram

Step 3

Insert the Wi-Fi antenna cable into the antenna hole from the inside of the panel I/O plate. Insert the toothed washer, fasten it with the nut and install the external antenna.

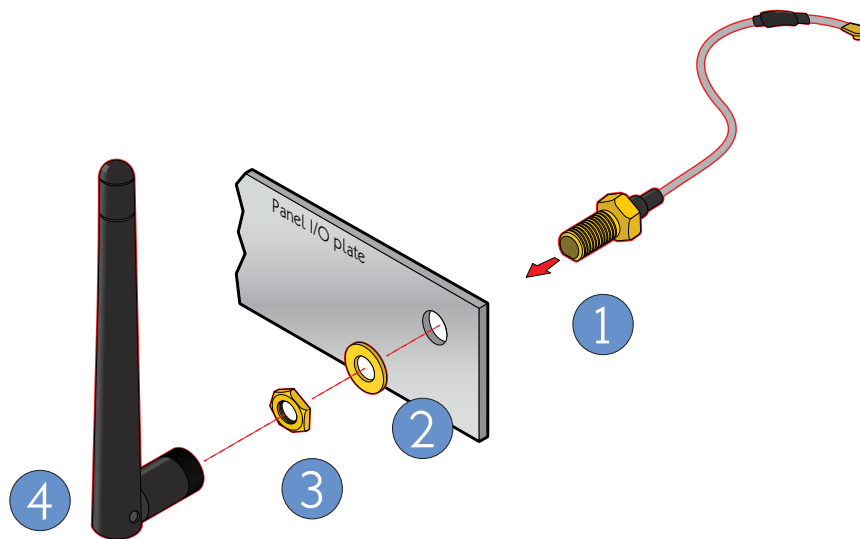


Figure 64: Installing Wi-Fi antenna cable diagram

Step 4

Connect the other end of the Wi-Fi antenna cable to the micro-RF connector labeled "I-PEX" on the EMIO-1533 module.

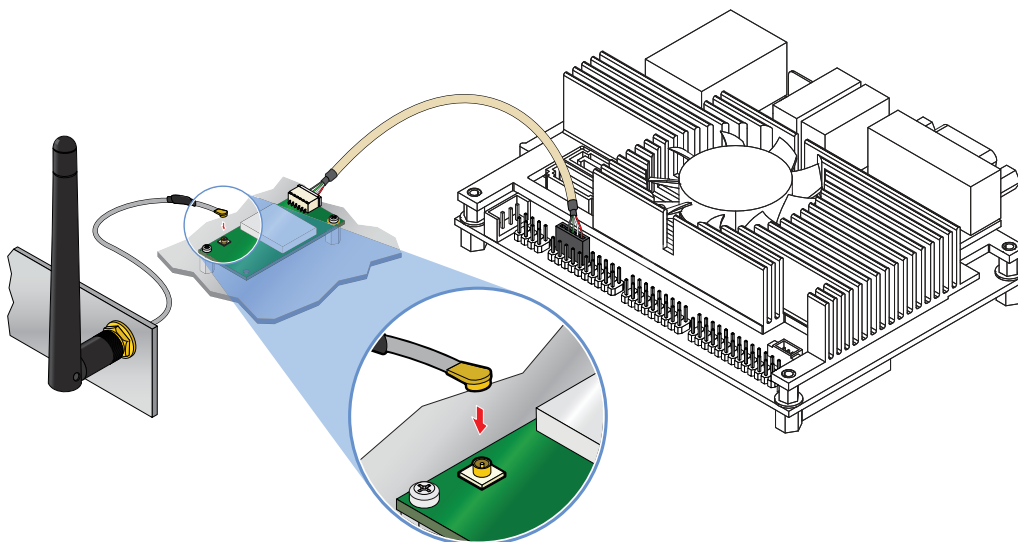


Figure 65: Connecting Wi-Fi antenna cable to the EMIO-1533 module

A.3. Installing the EMIO-5531 USB Wi-Fi + Bluetooth Module

Step 1

Mount the EMIO-5531 to the prepared standoff in the chassis. Align the two mounting holes on the EMIO-5531 module with the mounting holes on the standoffs. And then secure the EMIO-5531 module in place with two screws.

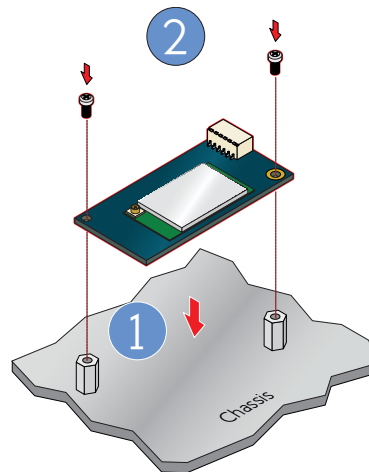


Figure 66: Installing EMIO-5531 USB Wi-Fi + BT module

Step 2

Connect one end of USB Wi-Fi cable to pin 4, 6, 8 and 10 of USB and USB Device Combination pin header (CN2) on EPIA-P900 board, and then connect the other end of the cable to the EMIO-5531 module.

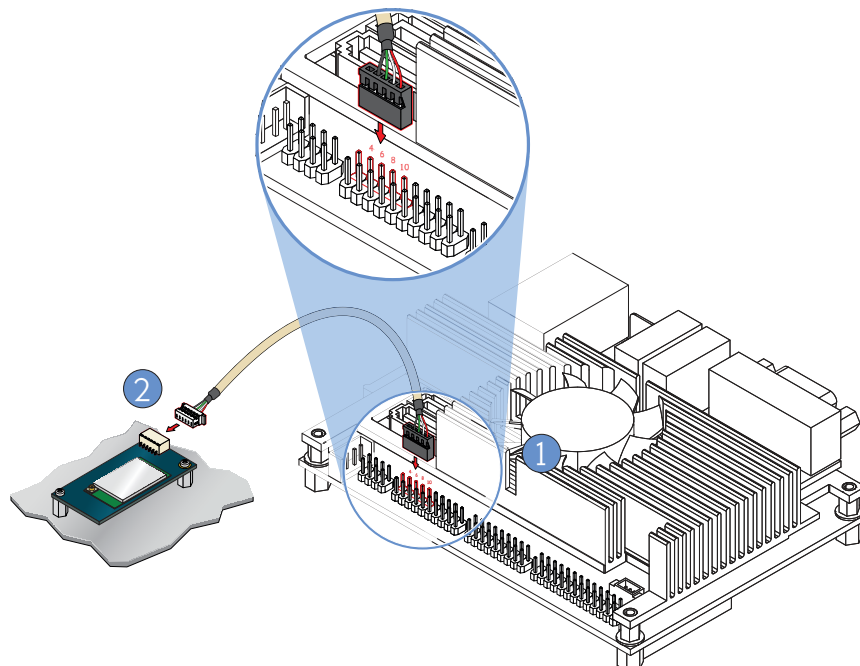


Figure 67: Connecting the USB Wi-Fi cable diagram

Step 3

Insert the Wi-Fi antenna cable into the antenna hole from the inside of the panel I/O plate. Insert the toothed washer, fasten it with the nut and install the external antenna.

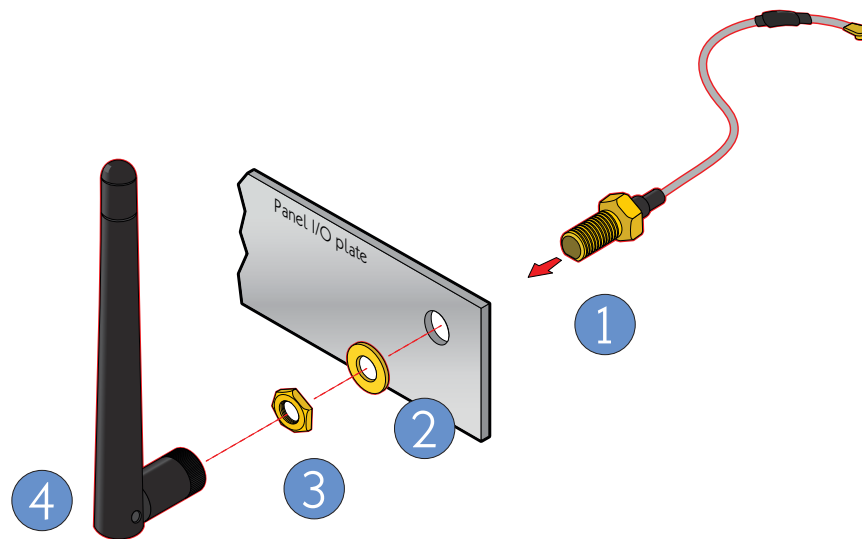


Figure 68: Installing Wi-Fi antenna cable diagram

Step 4

Connect the other end of the Wi-Fi antenna cable to the micro-RF connector labeled "I-PEX" on the EMIO-5531 module.

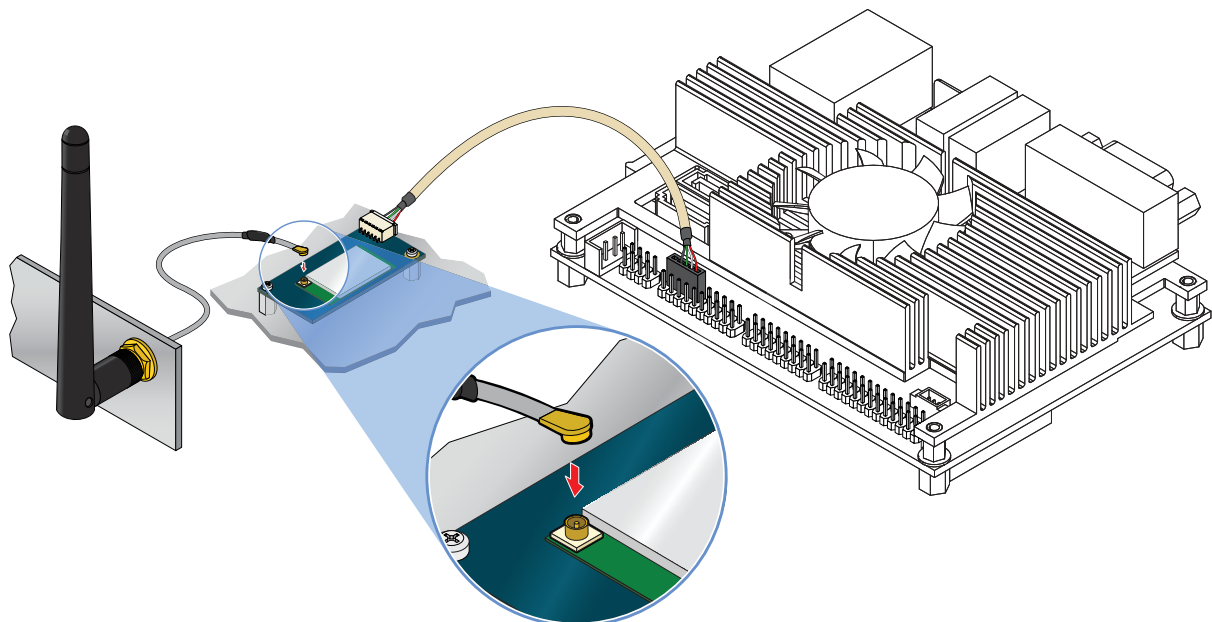


Figure 69: Connecting Wi-Fi antenna cable to the EMIO-5531 module

Appendix B. Power Consumption Report

Power consumption tests were performed on the VIA EPIA-P900. The following tables represent the breakdown of the voltage, ampere and wattage values while running common system applications.

B.1. EPIA-P900-10

The tests were performed based on the following additional components:

- **CPU:** VIA Eden® X2 1.0+ GHz (U4200 @1.0+ GHz)
- **Chipset:** VX900
- **Memory:** Transcend TS512MSK64V3N-I/4GB DDR3 SODIMM
- **HDD SATA:** Hitachi HTS545050B9A300 500GB (+5V/700mA)
- **Power supply:** PowDec Wl60-12V

Input: 100~240VAC, 50-60Hz, 1.7A

Output: +12VDC/5000mA

- **Operating System:** Windows 7 + SP1 x64

B.1.1. Burn-in 3DMark06, 1280 x 1024 (Demo mode)

Test Condition	Volts	Amperes	Watts
Maximum	12.16	2.164	26.314
Average	12.09	1.345	16.261
Minimum	11.56	1.104	12.762

B.1.2. PassMark Burn-in (CPU usage = 100%)

Test Condition	Volts	Amperes	Watts
Maximum	12.01	2.176	26.134
Average	12.00	1.932	23.184
Minimum	11.96	1.788	21.384

B.1.3. Power DVD 10 to Player H.264 1080i_10M Movie

Test Condition	Volts	Amperes	Watts
Maximum	12.16	2.084	25.341
Average	12.06	1.489	17.957
Minimum	11.56	1.040	12.022

B.1.4. Power DVD 10 to Player MPEG2 1080P_40M Movie

Test Condition	Volts	Amperes	Watts
Maximum	12.16	2.112	25.682
Average	12.10	1.444	17.472
Minimum	12.07	1.000	12.070

B.1.5. Idle at Windows 7 x64

Test Condition	Volts	Amperes	Watts
Maximum	12.16	1.98	24.077
Average	12.12	1.052	12.750
Minimum	12.00	0.948	11.376

B.1.6. Suspend S1

Test Condition	Volts	Amperes	Watts
Maximum	12.20	0.640	7.808
Average	12.19	0.633	7.716
Minimum	12.16	0.624	7.588

B.1.7. Suspend S3

Test Condition	Volts	Amperes	Watts
Maximum	12.28	0.092	1.130
Average	12.23	0.086	1.052
Minimum	12.00	0.080	0.960

B.1.8. Suspend S4

Test Condition	Volts	Amperes	Watts
Maximum	12.28	0.076	0.933
Average	12.27	0.074	0.908
Minimum	12.24	0.072	0.881

B.1.9. Suspend S5

Test Condition	Volts	Amperes	Watts
Maximum	12.28	0.076	0.933
Average	12.26	0.073	0.895
Minimum	12.24	0.072	0.881

Appendix C. Pin Header and Connector Vendor Lists

The following tables listed the pin headers and connectors vendor lists of EPIA-P900 mainboard.

C.1. EPIA-P900 Mainboard

Label	Function	Pins	Vendor	Part No.
LVDS1	LVDS panel connector	24	ACES	87216-2416-06
SODIMM1	Memory slot	204	Foxconn	AS0A626-J6RG-7H
SATA1, SATA2	SATA connector	7	Win Win	WATM-07DBN4A2B8UW
FAN1	Fan connector	3	Neltron	1251S-03-SM1-TR-F5
BAT1	CMOS battery connector	2	Neltron	1251R-02-SM1-TR-F5
VGA_USB1	VGA and USB combination pin header	18	Neltron	2208SM-18G-BK-CP
PWR1	DC-in connector	2	Neltron	2317SJ-02-F4
PWR2	SATA power connector	3	Neltron	2317SEH-03
J1	UART port connector 2	10	Neltron	1600R-10-SM-TR
J2	UART port connector 1	12	Neltron	1600R-12-SM-TR
J3	SPI flash connector	8	Neltron	1600S-08-SM-TR
CN1	Front audio pin header	8	Neltron	2208SM-08G-BK-CP
CN2	USB and USB Device port combination connector	20	Neltron	2208SM-20G-BK-CP
CN3	Gigabit Ethernet pin header (LAN)	14	Neltron	2208SM-14G-BK-CP
CN4	Front panel and PS/2 combination pin header	18	Neltron	2208SM-18G-BK-CP
CN5	LPC, SMBus, and GPIO combination pin header	26	Neltron	2208SM-26G-BK-CP
CN6	PCIe and USB combination connector	80	Samtec	ERM8-040-01-L-D-EM2-TR
JM1	Clear CMOS jumper	3	Neltron	2199SA-03G-301523
JM2	LVDS panel power select jumper	3	Neltron	2199SA-03G-301523
JM3	Backlight power select jumper	3	Neltron	2199SA-03G-301523

Table 28: EPIA-P900 pin header and connector vendor lists

C.2. Mating Connector Vendor Lists

Connectors	Part No.	Mating Vendor & P/N	
DC IN	99G30-021525	Neltron	JST
		2318HJ-02	XH Series
F_Audio	99G30-05419I	Neltron	SAMTEC
		2207R-XXG	MMS-1XX-01-XX-DV
		2207S-XXG	N/A
		2207SM-XXG-45	TLE-1XX-01-XX-DV
		Neltron	SAMTEC SSW Series
		2214S-XXG-85	
		2214R-XXG-85	
F_PANEL	99G30-05351I	Neltron	SAMTEC SSW Series
		2208S-XXG	
		2208R-XXG	
		2208SM-XXG	
FAN	99G30-021115	Neltron	MOLEX
		1250HM-03	51021
LVDS	99G30-170022	ACES	JST
		87219-2400	SHDR-XXV-S-B
SATA PWR	99G30-021755	Neltron	JST
		2318HEX-03	EH Series
USB	99G30-05259I	Neltron	SAMTEC
		2207S-XXG	MMS-1XX-01-XX-DV
		2207R-XXG	N/A
		2207SM-XXG-45	TLE-1XX-01-XX-DV

Table 29: EPIA-P900 mating connector vendor lists



Taiwan Headquarters

1F, 531 Zhong-zheng Road,
Xindian Dist., New Taipei City 231
Taiwan

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Fax: 886-2-2218-9860
Email: embedded@via.com.tw



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USA

Tel: 1-510-687-4688
Fax: 1-510-687-4654
Email: embedded@viatech.com



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Tel: 81-3-5466-1637
Fax: 81-3-5466-1638
Email: embedded@viatech.co.jp



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Fax: 86-10-59852299
Email: embedded@viatech.com.cn



Europe

Email: embedded@via-tech.eu