VTS-8589

Low power quad-core digital signage starter kit for OPS systems
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Regulatory Compliance
FCC-A Radio Frequency Interference Statement
This equipment has been tested and found to comply with the limits for a class A digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his personal expense.

Notice 1
The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Notice 2
Shielded interface cables and A.C. power cord, if any, must be used in order to comply with the emission limits.

Notice 3
The product described in this document is designed for general use, VIA Technologies assumes no responsibility for the conflicts or damages arising from incompatibility of the product. Check compatibility issue with your local sales representatives before placing an order.
Battery Recycling and Disposal

- Only use the appropriate battery specified for this product.
- Do not re-use, recharge, or reheat an old battery.
- Do not attempt to force open the battery.
- Do not discard used batteries with regular trash.
- Discard used batteries according to local regulations.

Safety Precautions

- Always read the safety instructions carefully.
- All cautions and warnings on the equipment should be noted.
- Keep this equipment away from humidity.
- Lay this equipment on a reliable flat surface before setting it up.
- Make sure the voltage of the power source and adjust properly 110/220V before connecting the equipment to the power inlet.
- Place the power cord in such a way that people cannot step on it.
- Always unplug the power cord before inserting any add-on card or module.
- If any of the following situations arises, get the equipment checked by authorized service personnel:
  - The power cord or plug is damaged.
  - Liquid has penetrated into the equipment.
  - The equipment has been exposed to moisture.
  - The equipment has not worked well or you cannot get it work according to User's Manual.
  - The equipment has dropped and damaged.
  - The equipment has obvious sign of breakage.
- Do not leave this equipment in an environment unconditioned or in a storage temperature above 60°C (140°F). The equipment may be damaged.
- Do not leave this equipment in direct sunlight.
- Never pour any liquid into the opening. Liquid can cause damage or electrical shock.
- Do not place anything over the power cord.
- Do not cover the ventilation holes. The openings on the enclosure protect the equipment from overheating.
Box Contents

**VTS-8589-QP SKU**
- 1 x VTS-8589-QP board (with 1.0GHz NXPI i.MX 6QuadPlus Cortex-A9 quad-core SoC)
- 1 x Wi-Fi antenna
- 1 x Wi-Fi antenna cable

**VTS-8589 SKU**
- 1 x VTS-8589 board (with 1.0GHz NXPI i.MX 6Quad Cortex-A9 quad-core SoC)
- 1 x Wi-Fi antenna
- 1 x Wi-Fi antenna cable

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>00GFK10H10020</td>
<td>VTS-8589-QP board with 1.0GHz i.MX 6QuadPlus Cortex-A9 SoC, 8GB eMMC, 4MB SPI Flash ROM, 2GB DDR3 SDRAM, 2 x USB 2.0, Gigabit Ethernet, onboard Wi-Fi, Micro SD card slot, and OPS connector</td>
</tr>
<tr>
<td>00GFK10500020</td>
<td>VTS-8589 board with 1.0GHz i.MX 6Quad Cortex-A9 SoC, 8GB eMMC, 4MB SPI Flash ROM, 2GB DDR3 SDRAM, 2 x USB 2.0, Gigabit Ethernet, onboard Wi-Fi, Micro SD card slot, and OPS connector</td>
</tr>
</tbody>
</table>

Optional Accessories

**AC-to-DC Adapter and Power Cord**

<table>
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<tr>
<th>Part Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>99G63-020306</td>
<td>AC-to-DC adapter, DC 12V/5A, 60W</td>
</tr>
<tr>
<td>99G33-02032C</td>
<td>Power cord, 180cm, USA type</td>
</tr>
</tbody>
</table>

**Expansion Boards**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>00GJE00000020</td>
<td>VTS-8592 OPS I/O card with 2 x USB 2.0 ports, HDMI port, audio jack, reset, power button, and 12V–19V DC-in</td>
</tr>
</tbody>
</table>
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1. Product Overview

The VTS-8589 board is an all-in-one digital signage kit for Open Pluggable Specification (OPS) systems. Complies with OPS board standard, the VTS-8589 board is powered by a 1.0GHz NXP i.MX 6QuadPlus (or i.MX 6Quad) Cortex-A9 quad-core SoC with power efficient graphics processor to delivers remarkable performance for a wide range digital signage applications.

The VTS-8589 board is fully compatible with Linux kernel 4.1.15 / 3.10.53 operating system, and provides support for extensive connectivity options, including and Gigabit Ethernet port, two USB 2.0 ports, and Micro SD card slot for expandable storage. In addition, the VTS-8589 board has an onboard Wi-Fi controller for Wi-Fi connectivity.

1.1. Key Features

- Powered by 1.0GHz NXP i.MX 6QuadPlus (or i.MX 6Quad) Cortex-A9 quad-core SoC
- Open Pluggable Specification (OPS) compliant
- Onboard Wi-Fi support
- Flawless HD video performance up to 1080p
- 8GB onboard eMMC Flash memory
- Supports one Micro SD card slot for expandable storage up to 32GB size
- HTML5 support
- Compatible with Linux kernel operating system
  - Linux kernel 4.1.15 (VTS-8589-QP SKU)
  - Linux kernel 3.10.53 (VTS-8589 SKU)
- Fanless and ultra-low power consumption
1.2. Product Specifications

**Processor**
- 1.0GHz NXP i.MX 6QuadPlus Cortex-A9 quad-core SoC (VTS-8589-QP SKU)
- 1.0GHz NXP i.MX 6Quad Cortex-A9 quad-core SoC (VTS-8589 SKU)

**System Memory**
- 2GB DDR3 SDRAM

**Storage**
- 8GB eMMC Flash memory

**Boot Loader**
- 4MB SPI Flash ROM

**Graphics**
- Vivante GC2000+ GPU (VTS-8589-QP SKU) / Vivante GC2000 GPU (VTS-8589 SKU)
  - Three integrated, independent 3D/2D and video graphics processing units
  - Supports OpenGL® ES 3.0, OpenCL and OpenVG™ 1.1 hardware acceleration
  - Supports MPEG-2, VC1 and H.264 video decoding up to 1080p
  - Supports SD encoding

**LAN**
- Micrel KSZ9031RNX Gigabit Ethernet transceiver with RGMII support

**Audio**
- NXP SGTL5000 low power stereo codec

**HDMI**
- Integrated HDMI Transmitter

**USB**
- SMSC USB2514 USB 2.0 High Speed 4-port hub controller

**Wi-Fi**
- Realtek RTL 8189ETS SDIO onboard Wi-Fi module

**Onboard I/O**
- 1 x Boot Flash select jumper
- 1 x Watchdog select jumper
- 1 x RTC battery connector
- 1 x Wi-Fi antenna connector

**Front Panel I/O**
- 1 x Reset button
- 2 x USB 2.0 ports
- 1 x Gigabit Ethernet port
- 1 x Micro SD Card slot

**Back Panel I/O**
- 1 x Open Pluggable Specification (OPS) plug connector - includes signal for:
  - 2 x USB 2.0 ports
  - 1 x HDMI port
  - 1 x UART (TTL 3V3)
  - GPIO
  - Audio jack
  - Power (12V~19V)

**Power Supply**
- 12V~19V DC-in (included in OPS plug connector)
Operating Conditions
- Operating Temperature
  - 0°C ~ 45°C
- Operating Humidity
  - 0% ~ 90% (relative humidity; non-condensing)

Form Factor
- 121mm x 114mm (12.1cm x 11.4cm)

Operating System
- Linux kernel 4.1.15 (VTS-8589-QP SKU)
- Linux kernel 3.10.53 (VTS-8589 SKU)

Notes:
1. As the operating temperature provided in the specifications is a result of the test performed in VIA's chamber, a number of variables can influence this result. Please note that the working temperature may vary depending on the actual situation and environment. It is highly suggested to execute a solid testing and take all the variables into consideration when building the system. Please ensure that the system runs well under the operating temperature in terms of application.
2. Please note that the lifespan of the onboard eMMC memory chip may vary depending on the amount of access. More frequent and larger data access on eMMC memory makes its lifespan shorter. Therefore, it is highly recommended to use a replaceable external storage (e.g., Micro SD card) for large data access.
1.3. Layout Diagram

Figure 1: Layout diagram of the VTS-8589 (top side)

Figure 2: Layout diagram of the VTS-8589 (bottom side)
1.4. Product Dimensions

Figure 3: Mounting holes and dimensions of the VTS-8589 board

Figure 4: External I/O port dimensions of the VTS-8589 board (front panel)

Figure 5: External I/O port dimensions of the VTS-8589 board (back panel)
1.5. Height Distribution

Figure 6: Height distribution of the VTS-8589 board
2. I/O Interface

VTS-8589 has a selection of interfaces integrated into the board. It includes a selection of frequently used ports as part of the external I/O coastline.

2.1. External I/O Ports

![Front panel I/O](image1)

![Back panel I/O](image2)

2.1.1. Micro SD Card Slot

The Micro SD card slot offers expandable storage up to 32GB size.

![Micro SD card slot diagram](image3)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SD0DATA2</td>
</tr>
<tr>
<td>2</td>
<td>SD0DATA3</td>
</tr>
<tr>
<td>3</td>
<td>SD0CMD</td>
</tr>
<tr>
<td>4</td>
<td>VDD (3.3V)</td>
</tr>
<tr>
<td>5</td>
<td>SD0CLK</td>
</tr>
<tr>
<td>6</td>
<td>GND</td>
</tr>
<tr>
<td>7</td>
<td>SD0DATA0</td>
</tr>
<tr>
<td>8</td>
<td>SD0DATA1</td>
</tr>
<tr>
<td>9</td>
<td>SD0_CD</td>
</tr>
</tbody>
</table>

Table 1: Micro SD card slot pinout
2.1.2. Gigabit Ethernet Port

The VTS-8589 is equipped with Gigabit Ethernet port. The Gigabit Ethernet port is using 8 Position 8 Contact (8P8C) receptacle connector or commonly referred to as RJ-45. It is fully compliant with IEEE 802.3 (10BASE-T), 802.3u (100BASE-TX), and 802.3ab (1000BASE-T) standards. The pinout of the Gigabit Ethernet port is shown below.

![Gigabit Ethernet port diagram](image)

Table 2: Gigabit Ethernet port pinout

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LAN1_TD0+</td>
</tr>
<tr>
<td>2</td>
<td>LAN1_TD0-</td>
</tr>
<tr>
<td>3</td>
<td>LAN1_TD1+</td>
</tr>
<tr>
<td>4</td>
<td>LAN1_TD1-</td>
</tr>
<tr>
<td>5</td>
<td>LAN1_TD2+</td>
</tr>
<tr>
<td>6</td>
<td>LAN1_TD2-</td>
</tr>
<tr>
<td>7</td>
<td>LAN1_TD3+</td>
</tr>
<tr>
<td>8</td>
<td>LAN1_TD3-</td>
</tr>
</tbody>
</table>

The Gigabit Ethernet port (RJ-45) is equipped with two LED indicators on the front side to show its Active/Link status and Speed status.

<table>
<thead>
<tr>
<th>Speed</th>
<th>Active LED (Left LED on RJ-45 port)</th>
<th>Link LED (Right LED on RJ-45 port)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10Mbit</td>
<td>LED is off</td>
<td>LED is off</td>
</tr>
<tr>
<td>100Mbit</td>
<td>Flash in Orange color</td>
<td>The LED is always on in Red color</td>
</tr>
<tr>
<td>1000Mbit</td>
<td>Flash in Orange color</td>
<td>The LED is always on in Green color</td>
</tr>
</tbody>
</table>

Table 3: Gigabit Ethernet port LED color definition

2.1.3. USB 2.0 Port

The VTS-8589 is equipped with two USB 2.0 ports. Each port gives complete Plug & Play and hot swapping capability for external devices. The USB interface complies with USB UHCI, Rev. 2.0. The pinout of the USB 2.0 ports are shown below.

![USB 2.0 port diagram](image)

Table 4: USB 2.0 port pinout

<table>
<thead>
<tr>
<th>USB 2.0 port 1 Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VCC</td>
</tr>
<tr>
<td>2</td>
<td>USB1_data-</td>
</tr>
<tr>
<td>3</td>
<td>USB1_data+</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>USB 2.0 port 2 Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VCC</td>
</tr>
<tr>
<td>2</td>
<td>USB2_data-</td>
</tr>
<tr>
<td>3</td>
<td>USB2_data+</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
</tr>
</tbody>
</table>
2.1.4. Reset Button

The VTS-8589 comes with a reset button on the front panel. The reset button allows the user to reboot or reset the system forcibly without turning off the system power.

![Figure 12: Reset button diagram](image)

2.1.5. Open Pluggable Specification Plug Connector

The VTS-8589 comes with an Open Pluggable Specification (OPS) plug connector on the back panel that provides hardware interface connecting the OPS board to the OPS display or OPS I/O card. The OPS plug connector contains signal groups of HDMI, USB 2.0, UART, GPIO, audio, and power (12V–19V) interfaces. The OPS plug connector consists of 80-pins. The pinout of the OPS plug connector is shown below.

![Figure 13: OPS plug connector diagram](image)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>RSVD</td>
<td>41</td>
<td>RSVD</td>
</tr>
<tr>
<td>2</td>
<td>RSVD</td>
<td>42</td>
<td>RSVD</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>43</td>
<td>RSVD</td>
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<td>49</td>
<td>RSVD</td>
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<td>10</td>
<td>RSVD</td>
<td>50</td>
<td>SYS_FAN</td>
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<td>11</td>
<td>RSVD</td>
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<td>UART_RXD</td>
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<td>29</td>
<td>DVI_DDC_DATA</td>
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<td>AZ_LINEOUT_L</td>
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<tr>
<td>30</td>
<td>DVI_DDC_CLK</td>
<td>70</td>
<td>AZ_LINEOUT_R</td>
</tr>
<tr>
<td>Pin</td>
<td>Description</td>
<td>Value</td>
<td>Pin</td>
</tr>
<tr>
<td>-----</td>
<td>--------------</td>
<td>-------</td>
<td>-----</td>
</tr>
<tr>
<td>31</td>
<td>DVI_HPD</td>
<td></td>
<td>71</td>
</tr>
<tr>
<td>32</td>
<td>GND</td>
<td></td>
<td>72</td>
</tr>
<tr>
<td>33</td>
<td>+12V~+19V</td>
<td></td>
<td>73</td>
</tr>
<tr>
<td>34</td>
<td>+12V~+19V</td>
<td></td>
<td>74</td>
</tr>
<tr>
<td>35</td>
<td>+12V~+19V</td>
<td></td>
<td>75</td>
</tr>
<tr>
<td>36</td>
<td>+12V~+19V</td>
<td></td>
<td>76</td>
</tr>
<tr>
<td>37</td>
<td>+12V~+19V</td>
<td></td>
<td>77</td>
</tr>
<tr>
<td>38</td>
<td>+12V~+19V</td>
<td></td>
<td>78</td>
</tr>
<tr>
<td>39</td>
<td>+12V~+19V</td>
<td></td>
<td>79</td>
</tr>
<tr>
<td>40</td>
<td>+12V~+19V</td>
<td></td>
<td>80</td>
</tr>
</tbody>
</table>

Table 5: OPS plug connector pinout
2.2. Onboard Connectors

2.2.1. RTC Battery Connector
The VTS-8589 is equipped with onboard RTC battery connector used for connecting the external cable battery that provides power to the 32.768KHz crystal oscillator for Real Time Clock (RTC). The pinout of the RTC connector is shown below.

![RTC battery connector diagram](image)

**Figure 14: RTC battery connector diagram**

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VBAT</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
</tr>
</tbody>
</table>

**Table 6: RTC battery connector pinout**

2.2.2. Wi-Fi Antenna Connector
The VTS-8589 is equipped with an onboard Wi-Fi antenna connector (micro-RF connector) used for connecting the Wi-Fi antenna cable to provide Wi-Fi connection. The Wi-Fi antenna connector is labeled as "VCC_WIFI".

![Wi-Fi antenna connector diagram](image)

**Figure 15: Wi-Fi antenna connector diagram**
3. Onboard Jumpers

This section will explain how to configure the VTS-8589 board to match the needs of your application by setting the jumpers.

**Jumper Description**

A jumper consists of pair conductive pins used to close in or bypass an electronic circuit to set up or configure particular feature using a jumper cap. The jumper cap is a small metal clip covered by plastic. It performs like a connecting bridge to short (connect) the pair of pins. The usual colors of the jumper cap are black/red/blue/white/yellow.

**Basic Jumper Configuration**

There are two settings of the jumper pin: "Short" and "Open". The pins are "Short" when a jumper cap is placed on the pair of pins. The pins are "Open" if the jumper cap is removed.

In addition, there are jumpers that have three or more pins, and some pins are arranged in series. In case of a jumper with three pins, place the jumper cap on pin 1 and pin 2 or pin 2 and 3 to Short it.

Some jumper size is small or mounted on the crowded location on the board that makes it difficult to access. Therefore, using a long-nose pliers in installing and removing the jumper cap is very helpful.

![Jumper settings example](image)

**Figure 16: Jumper settings example**

**Caution:**
Make sure to install the jumper cap on the correct pins. Installing it in the wrong pin might cause damage and malfunction.
3.1. Boot Select Jumper

The boot select jumper labeled as “J11” is set to specify the boot device: SPI and Micro SD. The default setting is the SPI. The boot select jumper settings are shown below.

![Boot select jumper diagram](image)

<table>
<thead>
<tr>
<th>Setting</th>
<th>Pin 1</th>
<th>Pin 2</th>
<th>Pin 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPI (default)</td>
<td>Open</td>
<td>Short</td>
<td>Short</td>
</tr>
<tr>
<td>Micro SD</td>
<td>Short</td>
<td>Short</td>
<td>Open</td>
</tr>
</tbody>
</table>

Table 7: Boot select jumper settings

3.2. Watchdog Jumper

The watchdog jumper is used to enable or disable the watchdog function. The jumper is labeled as “J14”. The jumper settings are shown below.

![Watchdog jumper diagram](image)

<table>
<thead>
<tr>
<th>Setting</th>
<th>Pin 1</th>
<th>Pin 2</th>
<th>Pin 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Enable (default)</td>
<td>Open</td>
<td>Short</td>
<td>Short</td>
</tr>
<tr>
<td>Disable</td>
<td>Short</td>
<td>Short</td>
<td>Open</td>
</tr>
</tbody>
</table>

Table 8: Watchdog jumper settings
4. Hardware Installation

4.1. Installing the VTS-8592 OPS I/O card (optional)

The VTS-8592 is an optional OPS I/O card for VTS-8589 board. It can be connected through OPS connector.

Step 1
Align the OPS receptacle connector on VTS-8592 OPS I/O card with the OPS plug connector on the VTS-8589 board.

![Figure 19: Installing the VTS-8592 OPS I/O card](image)

Step 2
Gently connect the OPS receptacle connector into the OPS plug connector. There will be a slight tension as the OPS receptacle connector is being inserted.

Step 3
Secure the VTS-8592 OPS I/O card to the chassis with four screws.

![Figure 20: Securing the VTS-8592 OPS I/O card](image)

Note:
The VTS-8592 is an optional OPS I/O card. User can still install the VTS-8589 board with OPS chassis directly to the OPS display without using the VTS-8592 card.
5. Software and Technical Supports

5.1. Linux Support
The VTS-8589 is highly compatible with Linux kernel operating system.

- Linux kernel 4.1.15 (VTS-8589-QP SKU)
- Linux kernel 3.10.53 (VTS-8589 SKU)

5.1.1. Driver Installation
Linux Driver Support
Linux drivers are provided through various methods including:

- Drivers provided by VIA
- Using a driver built into a distribution package
- Visiting www.viatech.com for the latest updated drivers
- Installing a third party driver (such as the ALSA driver from the Advanced Linux Sound Architecture project for integrated audio)

5.2. Technical Supports and Assistance
- For technical support and additional assistance, always contact your local sales representative or board distributor, or go to http://www.viatech.com/en/about/contact/ to fill up the form request.
- For OEM clients and system integrators developing a product for long term production, other code and resources may also be made available. Contact VIA to submit a request.
Appendix A. VTS-8592 OPS I/O Card

The VTS-8592 is an optional OPS I/O card designed especially for VTS-8589 board. The VTS-8592 card can support two USB 2.0 ports, HDMI port, audio jack, rest button and power on/off button and DC-in jack.

A.1. VTS-8592 Specifications

Front Panel I/O
- 1 x Power On/Off button
- 1 x Audio jack
- 1 x HDMI port
- 2 x USB 2.0 ports
- 1 x DC-in jack (12V~19V)

Back Panel I/O
- 1 x OPS receptacle connector

Onboard I/O
- 1 x Power button connector
- 1 x System fan connector
- 1 x UART pin header (+3.3V)
- 1 x Power status pin header
- 1 x System fan LED
- 1 x Power LED
- 1 x Pluggable board detect LED

Dimensions
- 121.8mm x 45.6mm (12.18cm x 4.56cm)

Operating Temperature
- -0°C ~ 45°C

Operating Humidity
- 0 ~ 90% (relative humidity; non-condensing)
A.2. VTS-8592 Layout Diagram

![Layout Diagram of the VTS-8592 OPS I/O card]

Figure 21: Layout diagram of the VTS-8592 OPS I/O card

A.3. VTS-8592 Dimensions

![Mounting holes and dimensions of the VTS-8592 OPS I/O card (top side)]

Figure 22: Mounting holes and dimensions of the VTS-8592 OPS I/O card (top side)

![External I/O port dimensions of the VTS-8592 OPS I/O card (front panel)]

Figure 23: External I/O port dimensions of the VTS-8592 OPS I/O card (front panel)

![External I/O port dimension of the VTS-8592 OPS I/O card (back panel)]

Figure 24: External I/O port dimension of the VTS-8592 OPS I/O card (back panel)
A.4. VTS-8592 Height Distribution

Figure 25: Height distribution of the VTS-8592 OPS I/O card
A.5. VTS-8592 Connectors and LEDs

A.5.1. System Fan Connector

![System Fan Connector Diagram](image)

Table 9: System fan connector pinout

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>+12V</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
</tr>
</tbody>
</table>

A.5.2. Power Button Connector

![Power Button Connector Diagram](image)

Table 10: Power button connector pinout

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>+12V</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
</tr>
</tbody>
</table>

A.5.3. Power Status Pin Header

![Power Status Pin Header Diagram](image)

Table 11: Power status pin header pinout

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SYS_FAN (GPIO)</td>
</tr>
<tr>
<td>2</td>
<td>PB_DET</td>
</tr>
<tr>
<td>3</td>
<td>PWR_STATUS</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
</tr>
</tbody>
</table>
A.5.4. UART (3.3V) Pin Header

![UART Pin Header Diagram](image)

**Figure 29:** UART (3.3V) pin header diagram

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>NC</td>
</tr>
<tr>
<td>2</td>
<td>RXD</td>
</tr>
<tr>
<td>3</td>
<td>TXD</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
</tr>
</tbody>
</table>

**Table 12:** UART (3.3V) pin header pinout

A.5.5. Power LED

![Power LED Diagram](image)

**Figure 30:** Power LED diagram

A.5.6. System Fan LED

![System Fan LED Diagram](image)

**Figure 31:** System fan LED diagram

A.5.7. Pluggable Board Detect LED

![Pluggable Board Detect LED Diagram](image)

**Figure 32:** Pluggable board detect LED diagram
## Appendix B. Mating Connector Vendor Lists

The following tables listed the mating connector vendors of VTS-8589 board.

<table>
<thead>
<tr>
<th>Labels</th>
<th>Function</th>
<th>Vendor</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>J11</td>
<td>Boot select jumper</td>
<td>Neltron Industrial</td>
<td>2210SM-03G-B1-CP</td>
</tr>
<tr>
<td>J14</td>
<td>Watchdog jumper</td>
<td>Neltron Industrial</td>
<td>2210SM-03G-B1-CP</td>
</tr>
<tr>
<td>J100</td>
<td>OPS plug connector</td>
<td>JAE</td>
<td>TX25-80P-LT-H1E</td>
</tr>
<tr>
<td>J1</td>
<td>RTC battery connector</td>
<td>Neltron Industrial</td>
<td>1251R-02-SM1-TR-F5</td>
</tr>
<tr>
<td>J27</td>
<td>Wi-Fi antenna connector</td>
<td>ACES</td>
<td>50990-00108-001</td>
</tr>
<tr>
<td>J2</td>
<td>Micro SD Card slot</td>
<td>ATOM</td>
<td>MR01-AP20330</td>
</tr>
<tr>
<td>J16</td>
<td>Gigabit Ethernet port</td>
<td>Trxcom</td>
<td>TRJK0072AONL</td>
</tr>
<tr>
<td>USB0/1</td>
<td>USB 2.0 port</td>
<td>FOXCONN</td>
<td>UB1112C-8FDE-4F</td>
</tr>
<tr>
<td>SW4</td>
<td>Reset button</td>
<td>WKD</td>
<td>TS-018A</td>
</tr>
</tbody>
</table>

Table 13: VTS-8589 mating connector vendor lists